

FIG. 1
(Prior Art)

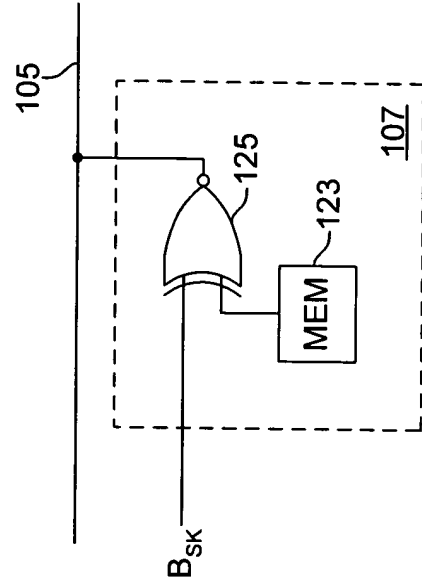


FIG. 2
(Prior Art)

FIG. 3

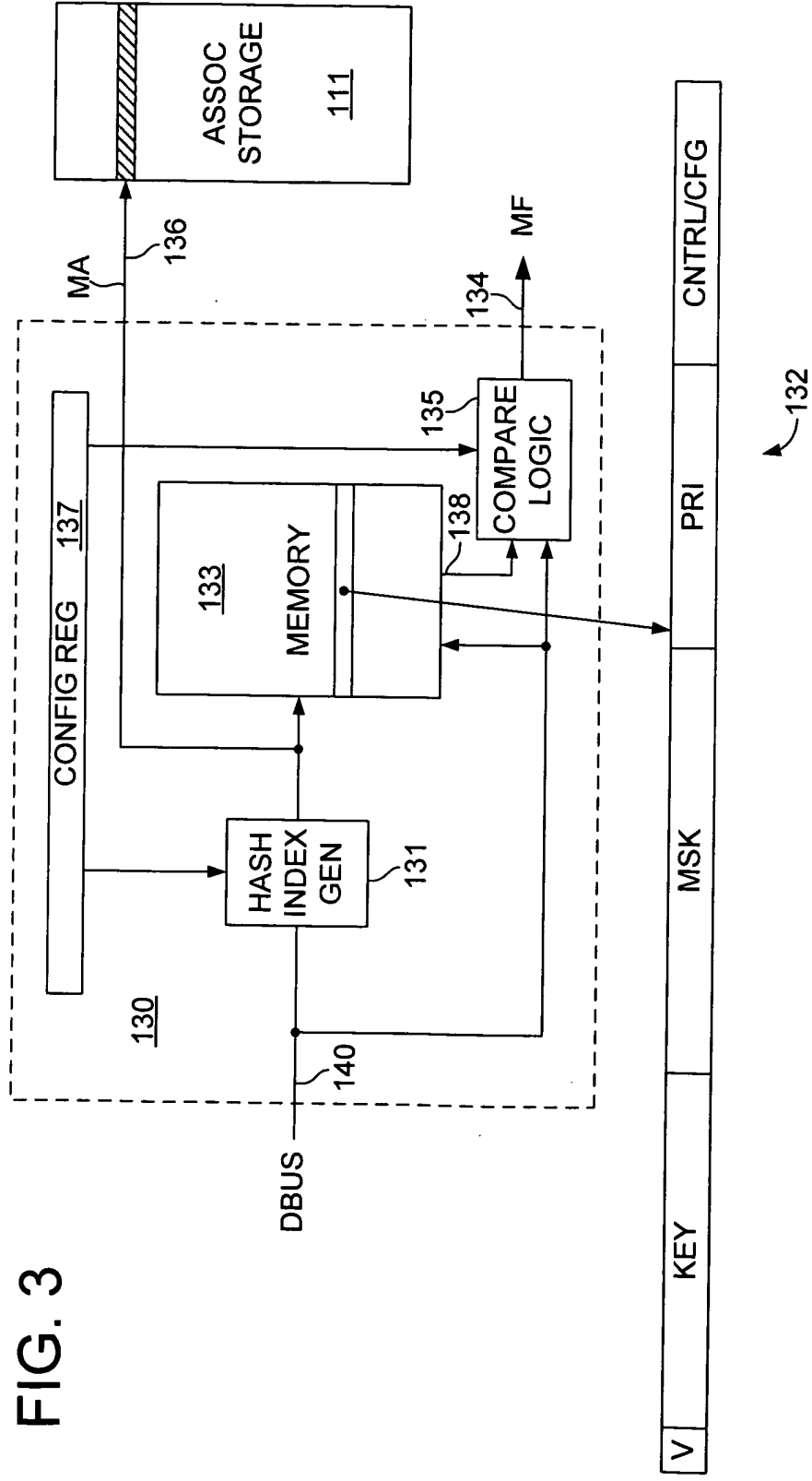


FIG. 4

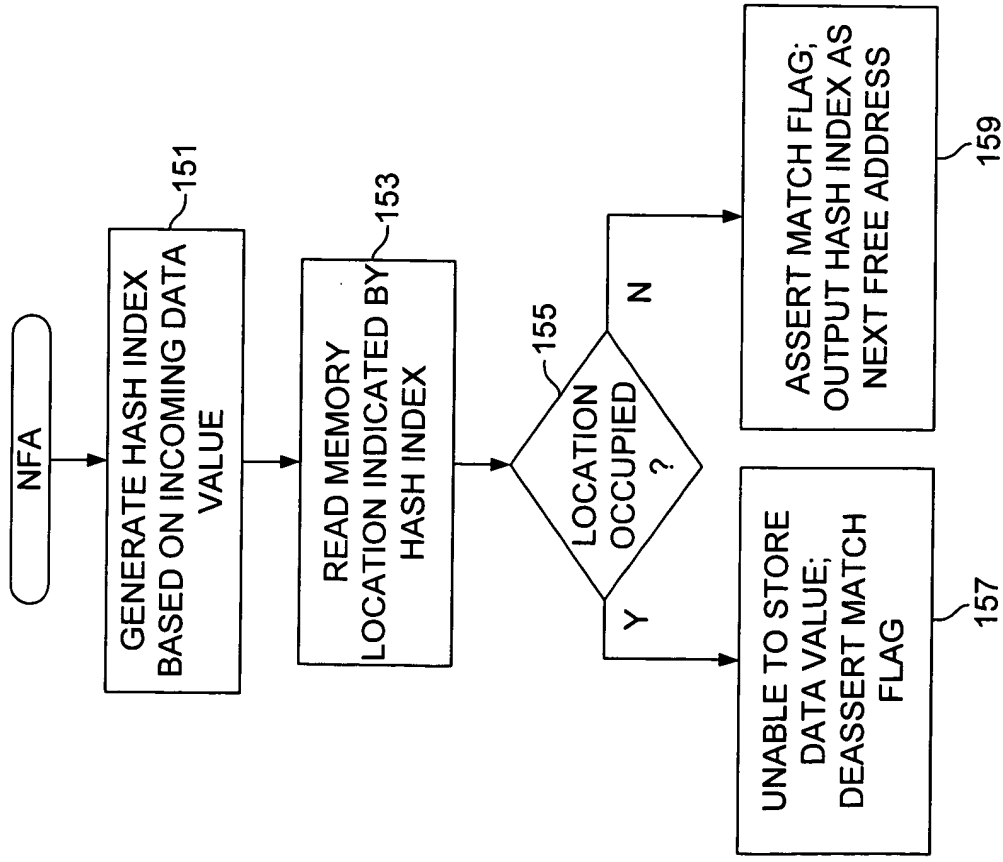
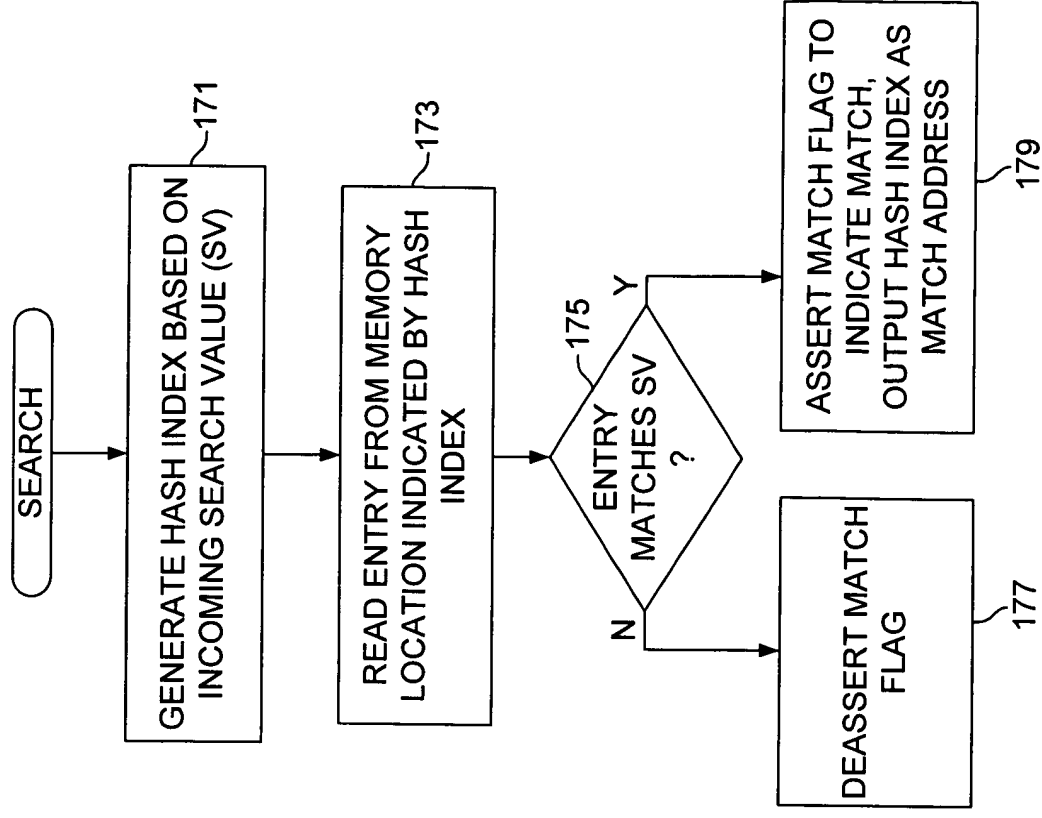


FIG. 5



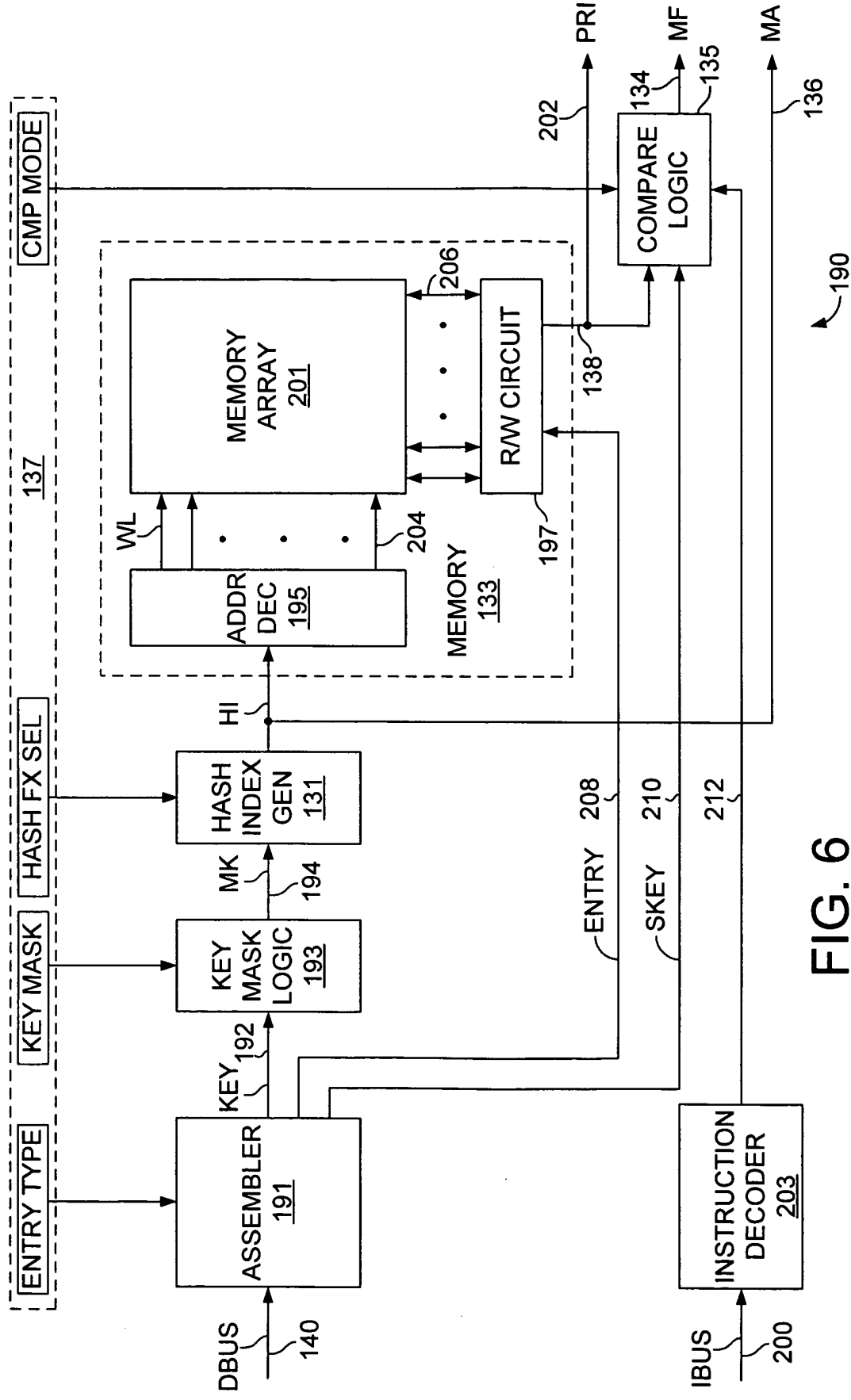
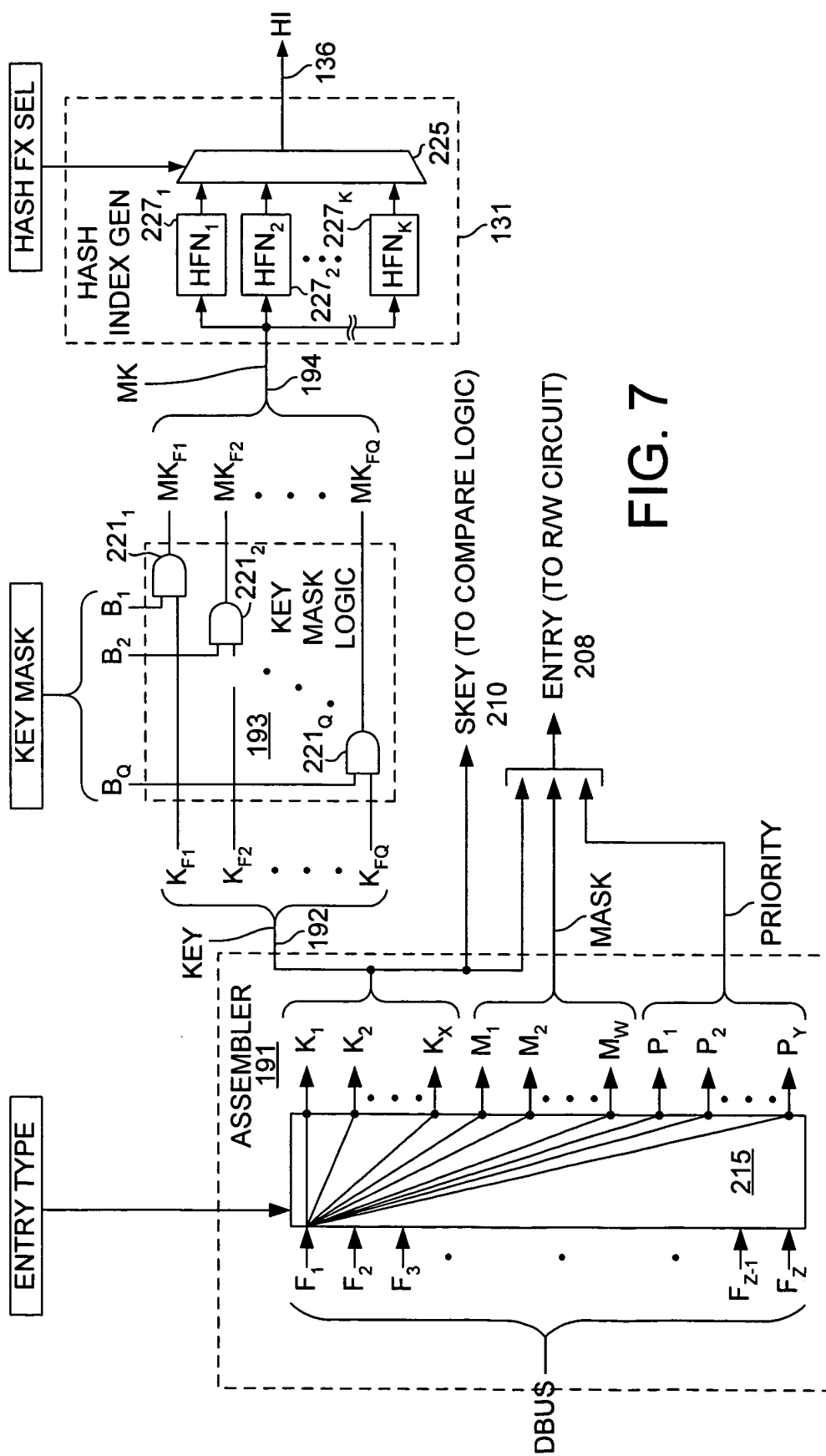


FIG. 6



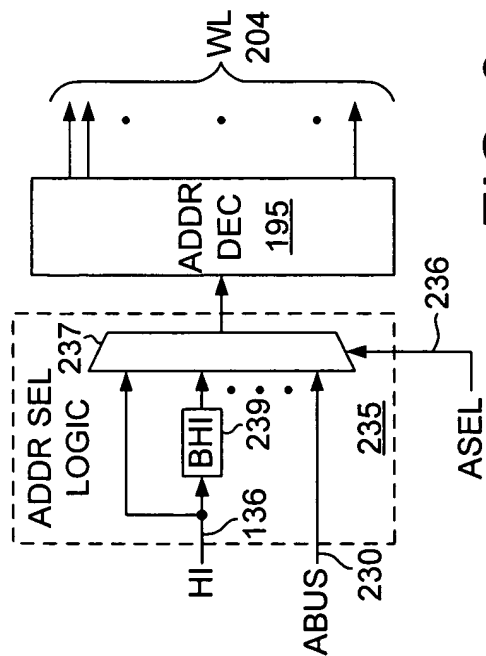


FIG. 8

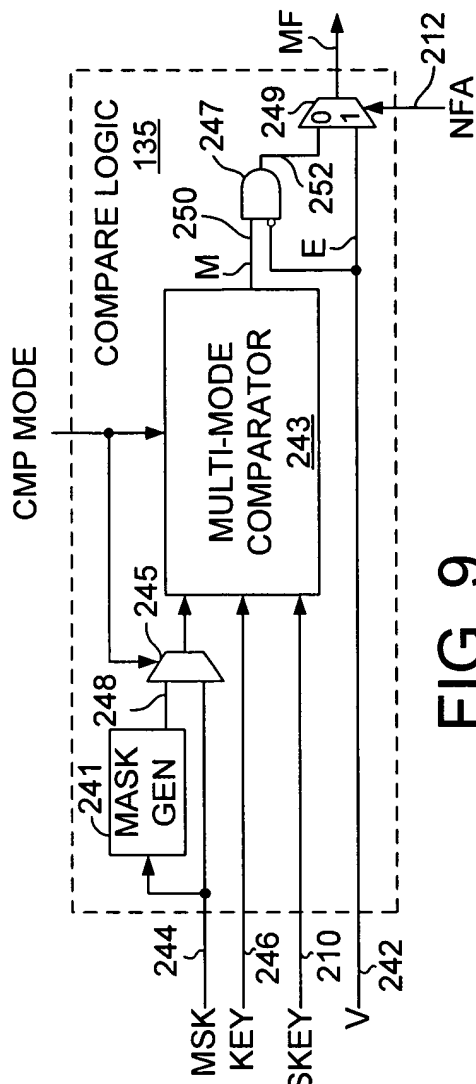


FIG. 9

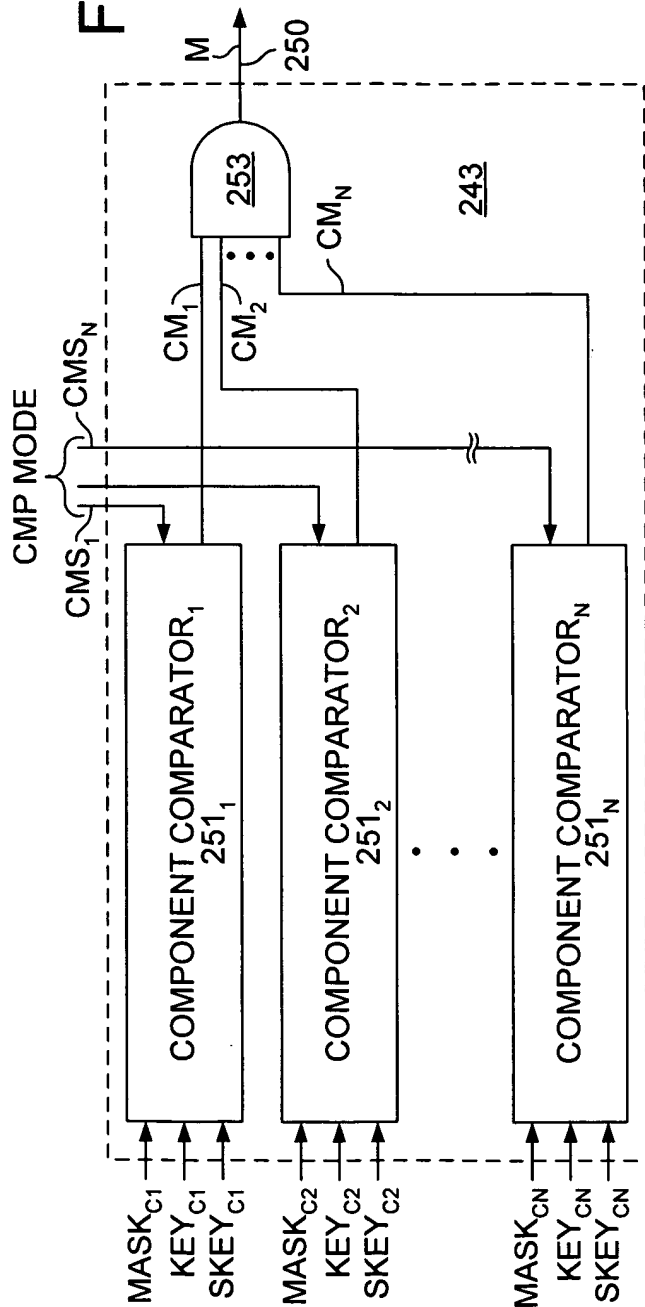


FIG. 10

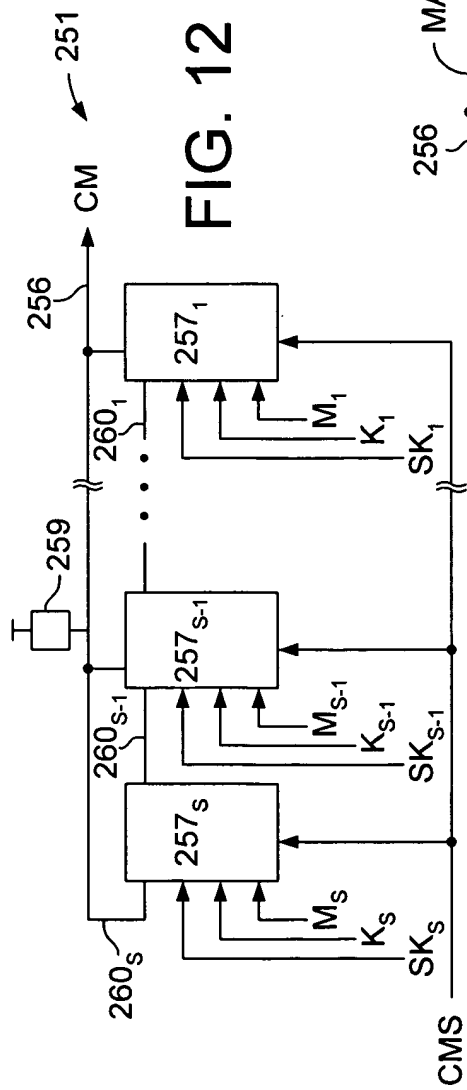


FIG. 12

FIG. 13

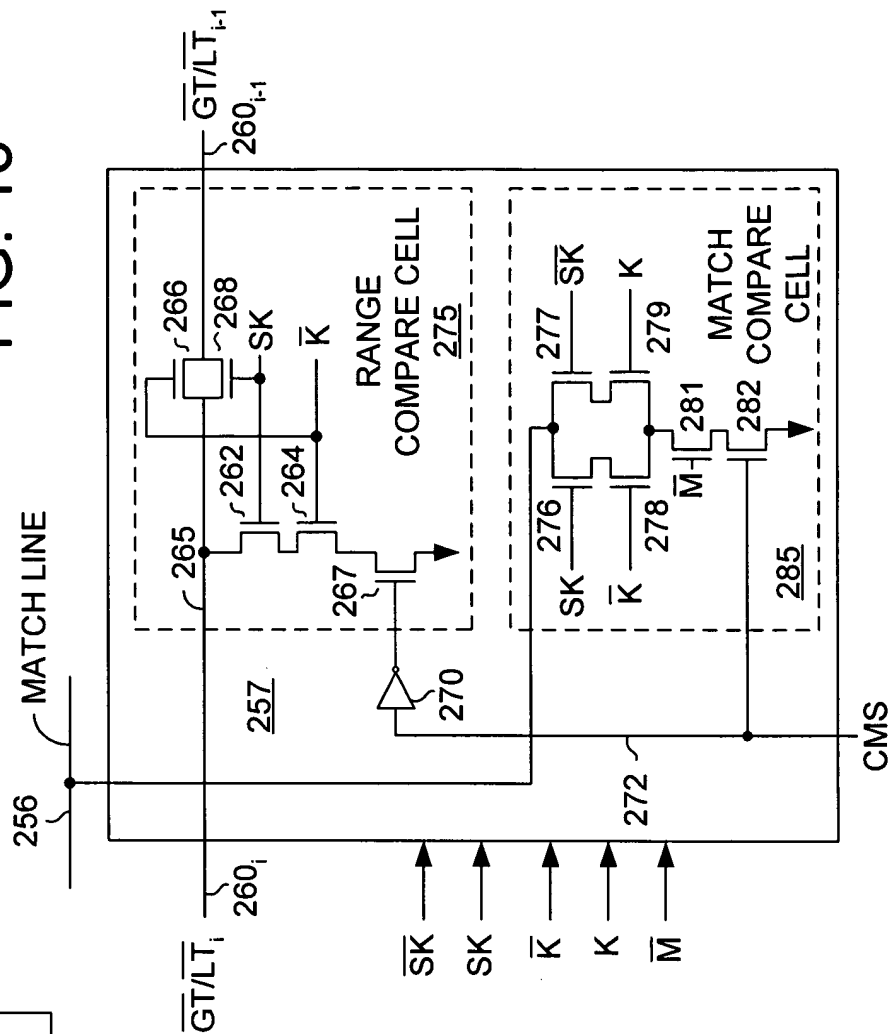
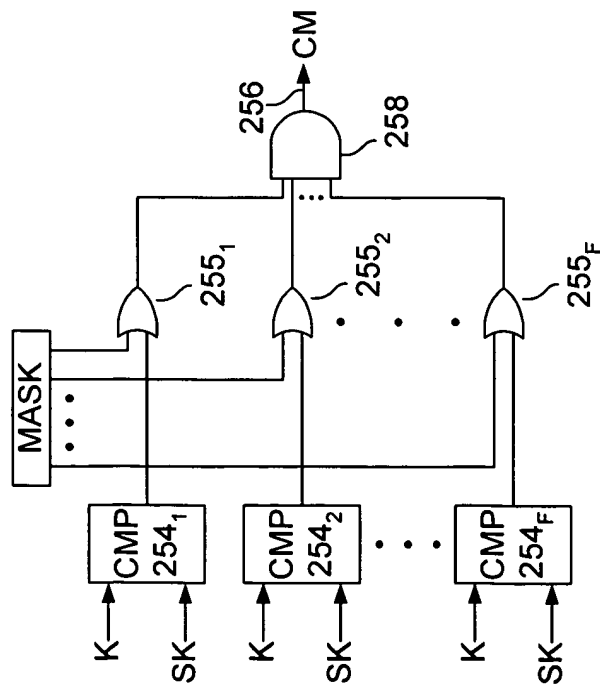


FIG. 11



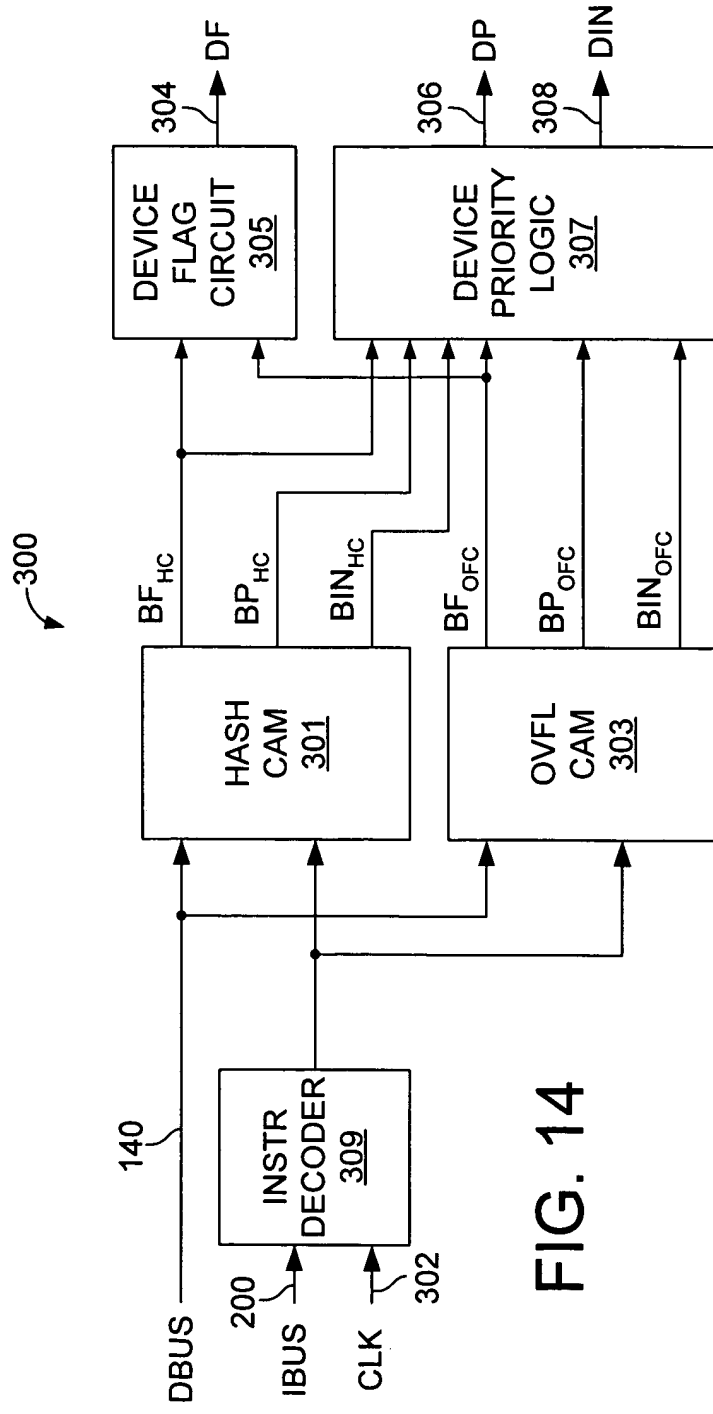


FIG. 14

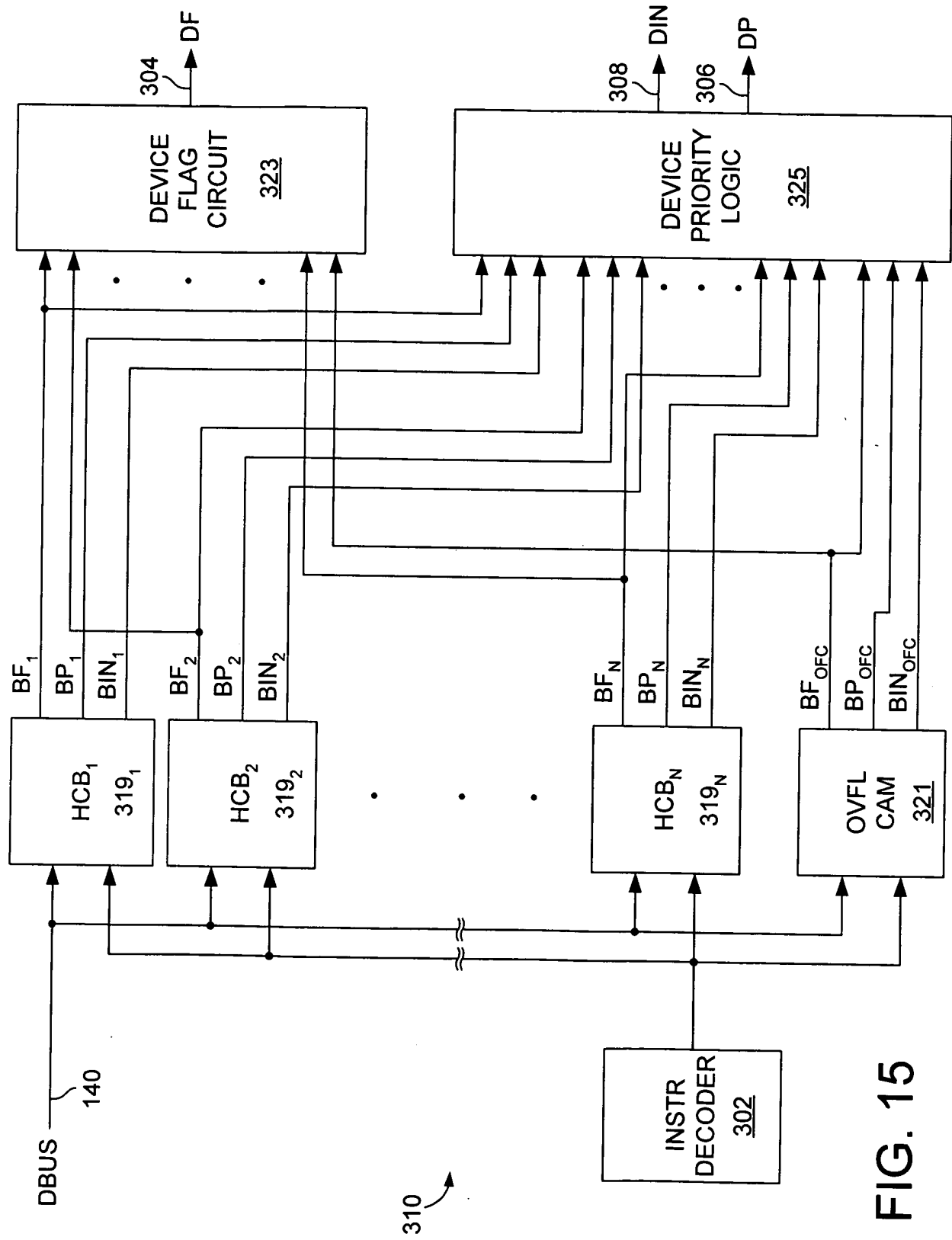


FIG. 15

FIG. 16 is a block diagram of a system 319, in accordance with an embodiment of the present invention. The system 319 includes an ASSEMBLER 191, a KEY MASK LOGIC 193, a HASH INDEX GEN 131, an ADDR SEL LOGIC 235, a MEMORY 133, a BLOCK SELECT LOGIC 331, an OUTPUT LOGIC 335, and an ENTRY 208. The system 319 is connected to a DBUS 140 and an NFA 212. The system 319 is configured to process an ENTRY TYPE, KEY MASK, HASH FX SEL, and CMP MODE to generate an output BIN 340.

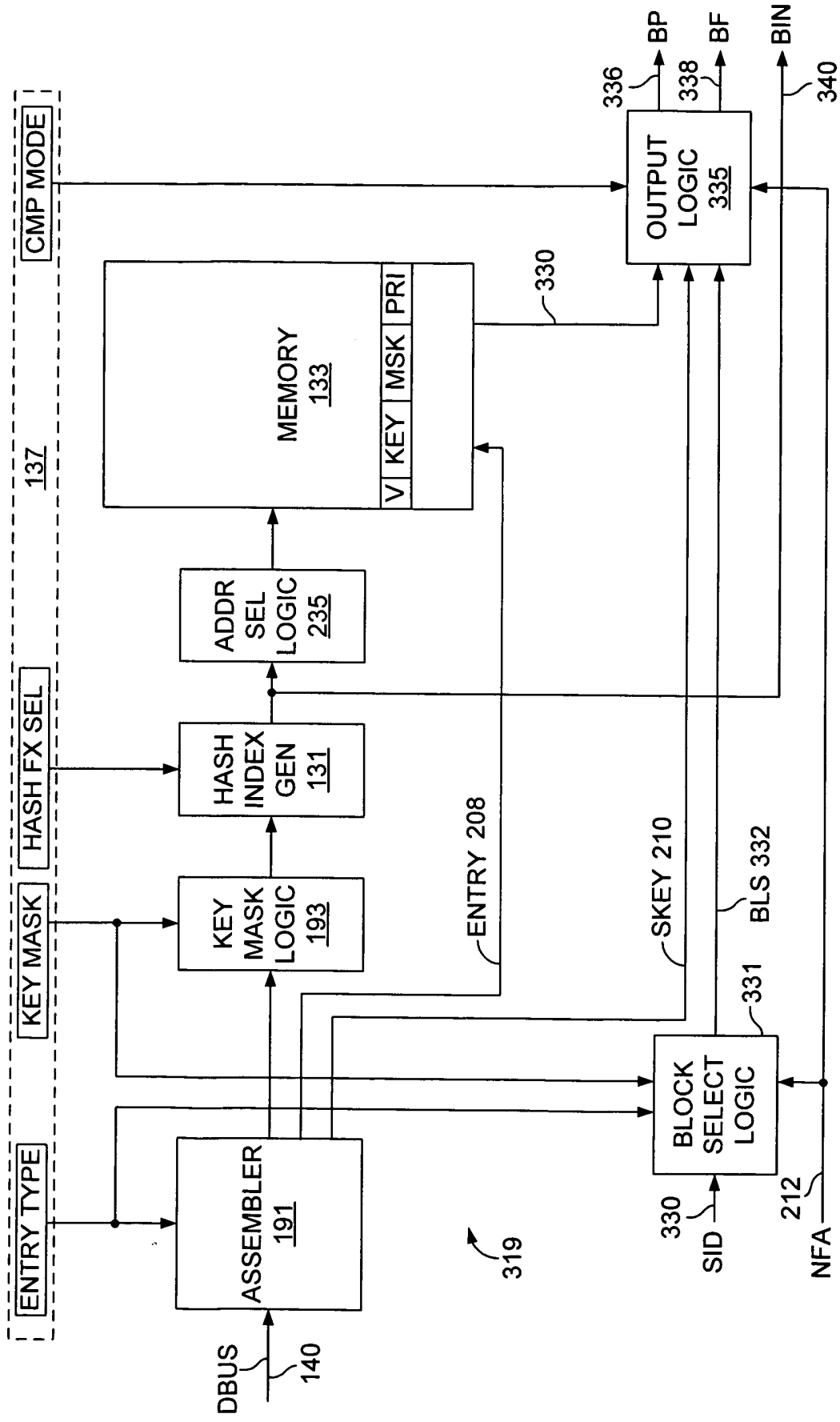


FIG. 16

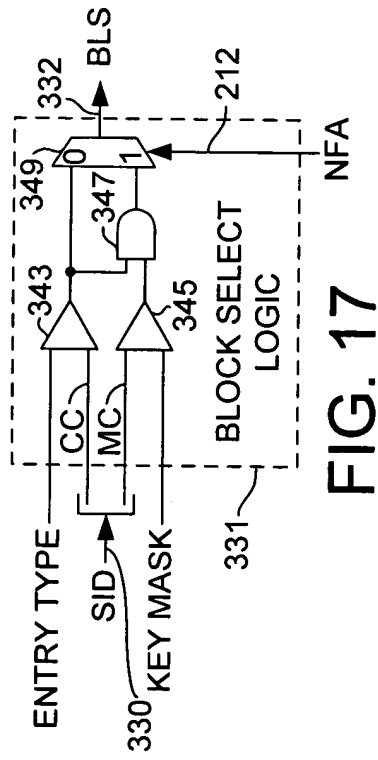
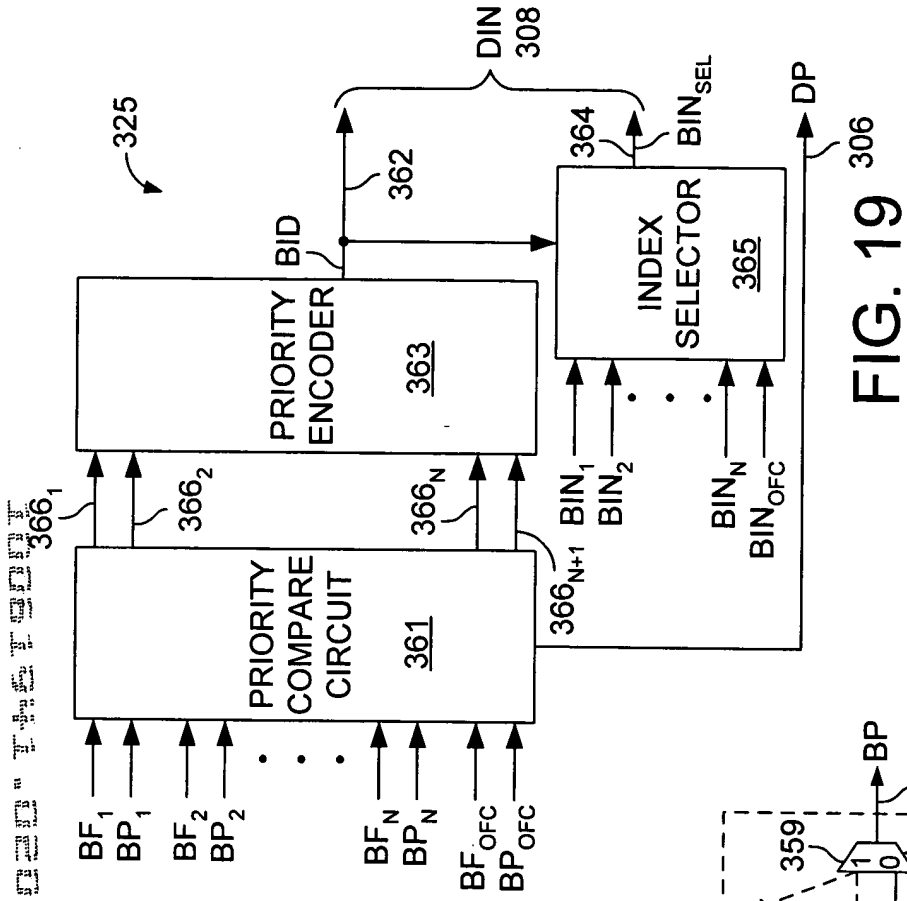
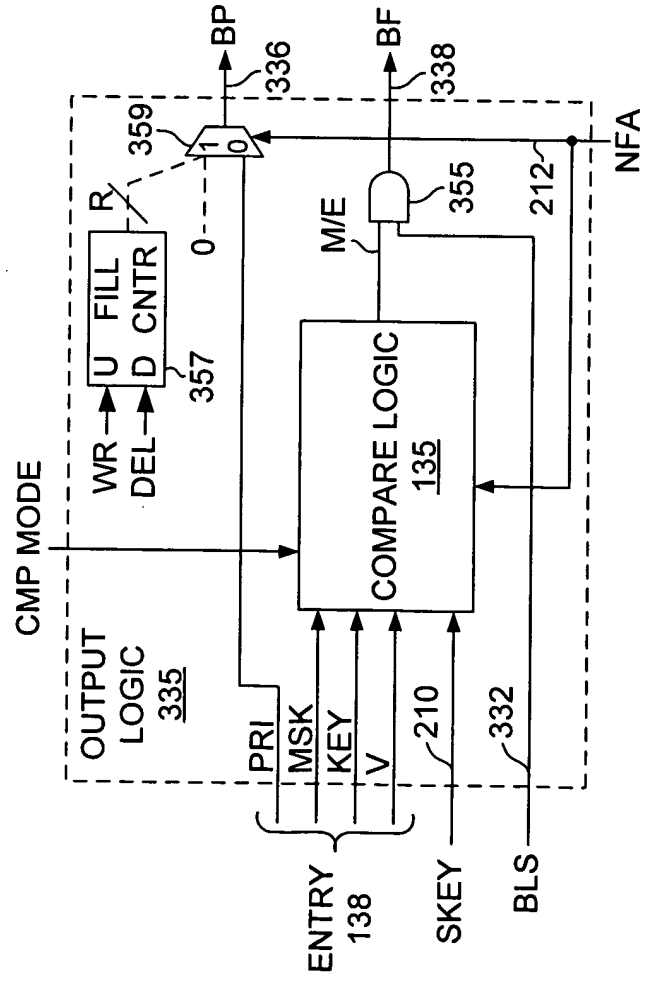


FIG. 18



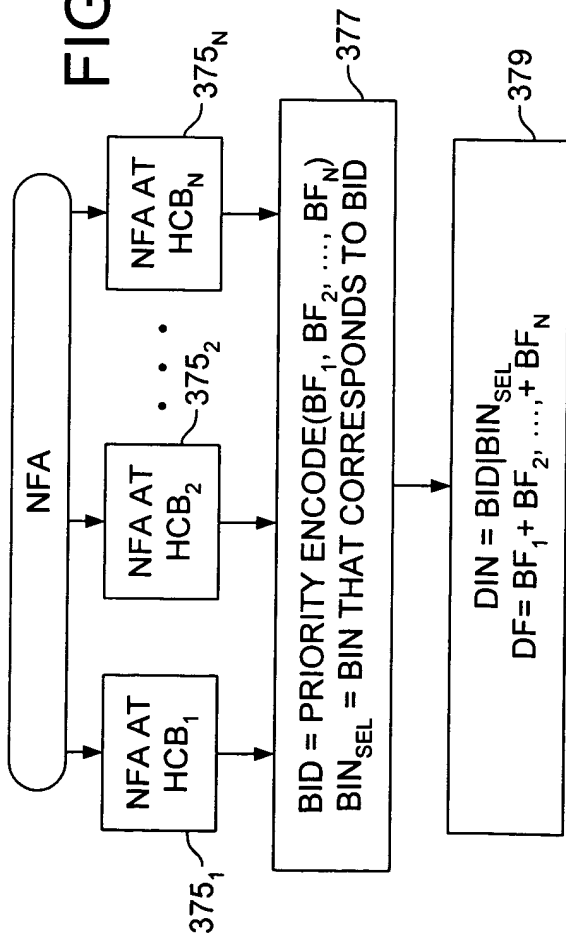


FIG. 20

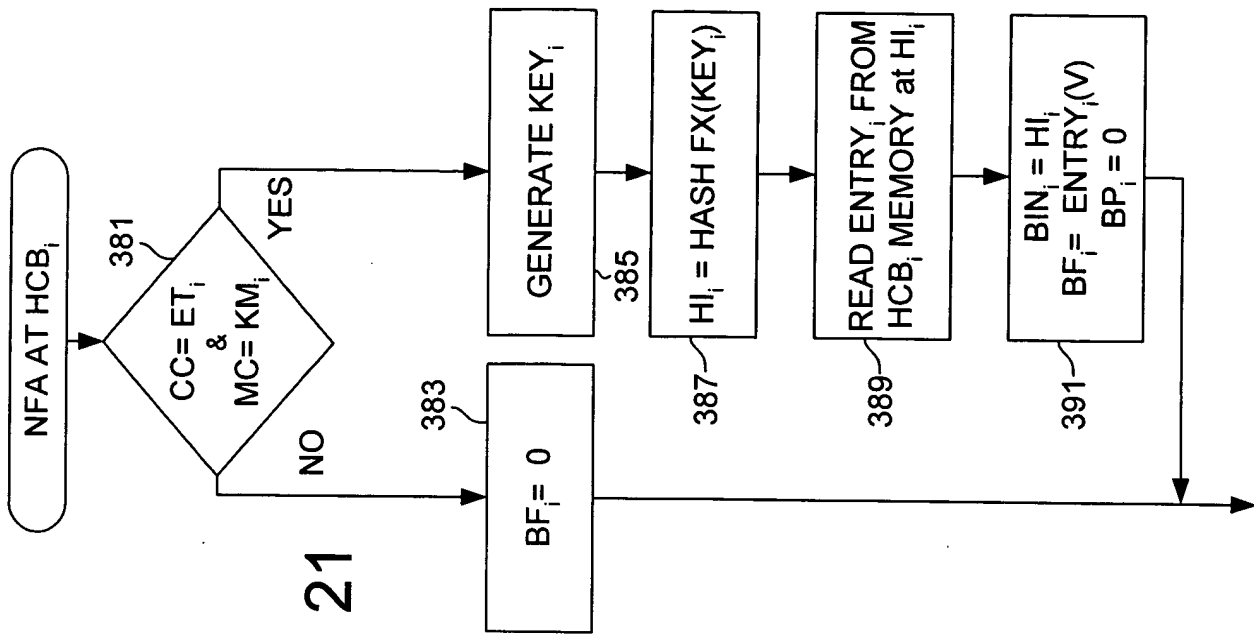


FIG. 21

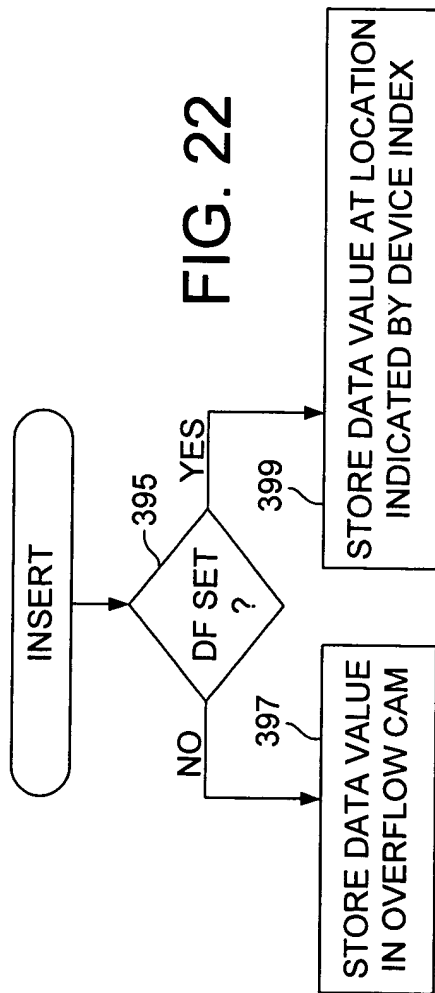


FIG. 22

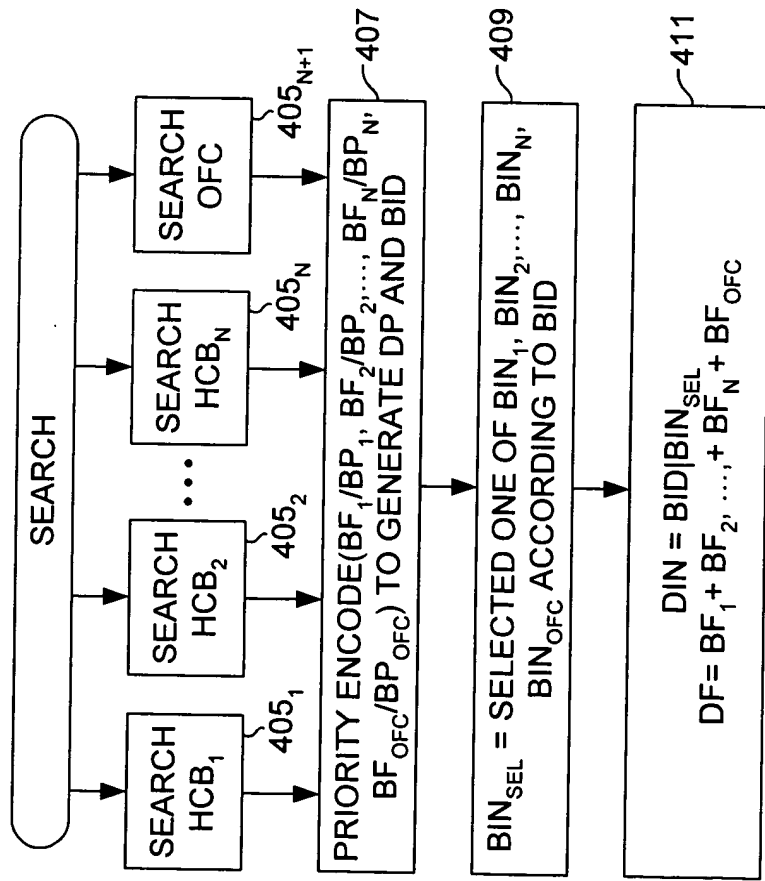


FIG. 23

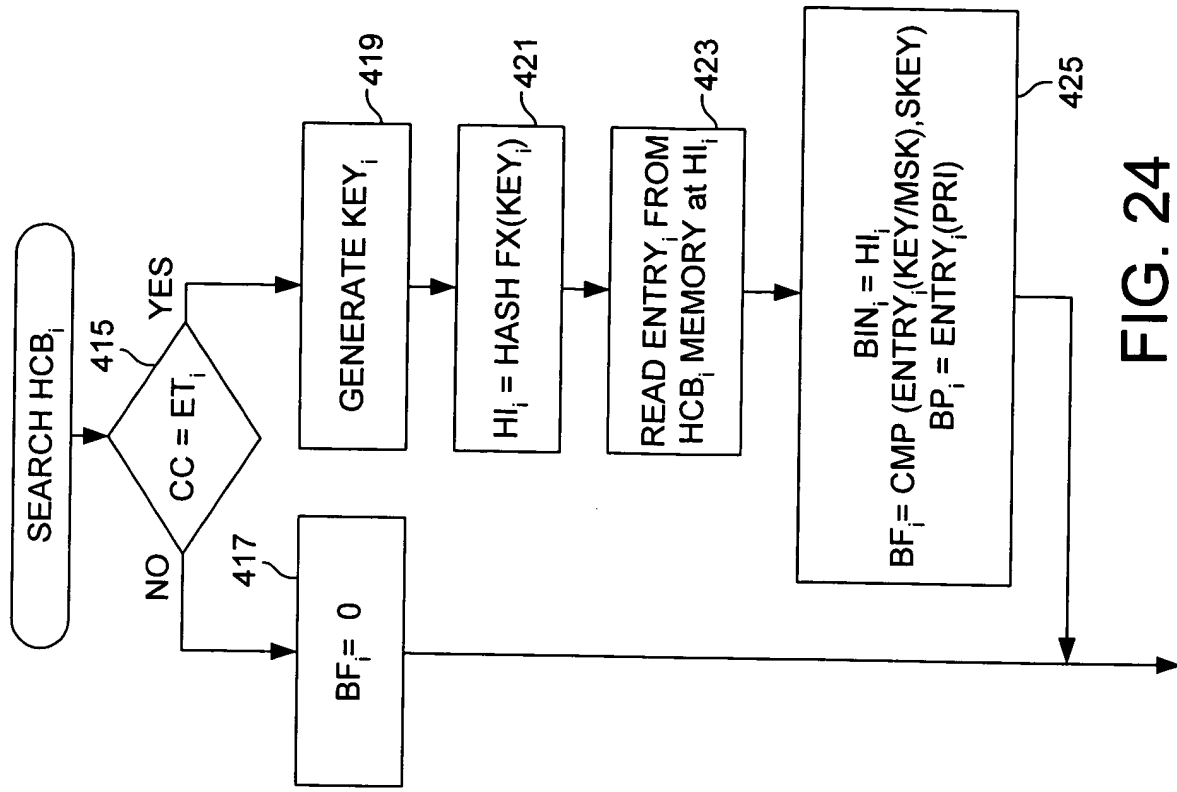


FIG. 24

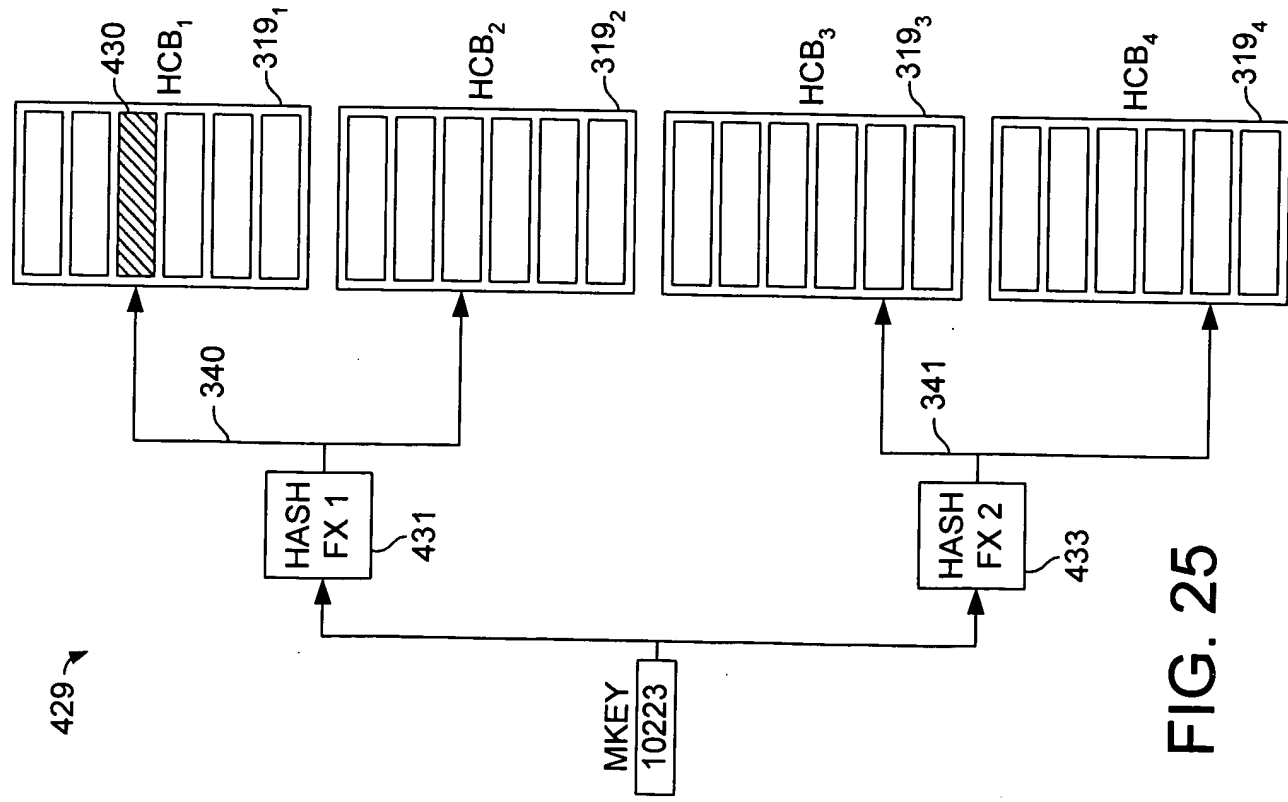


FIG. 25

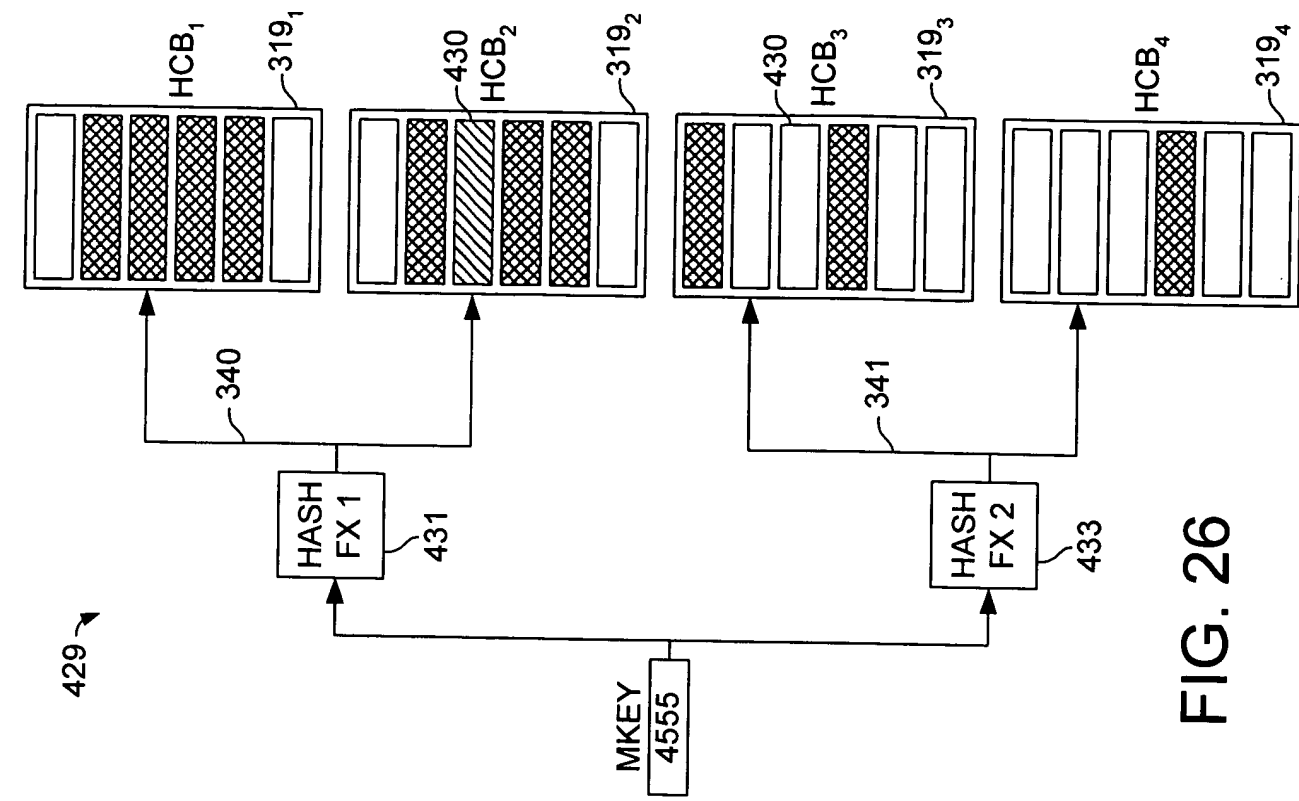


FIG. 26

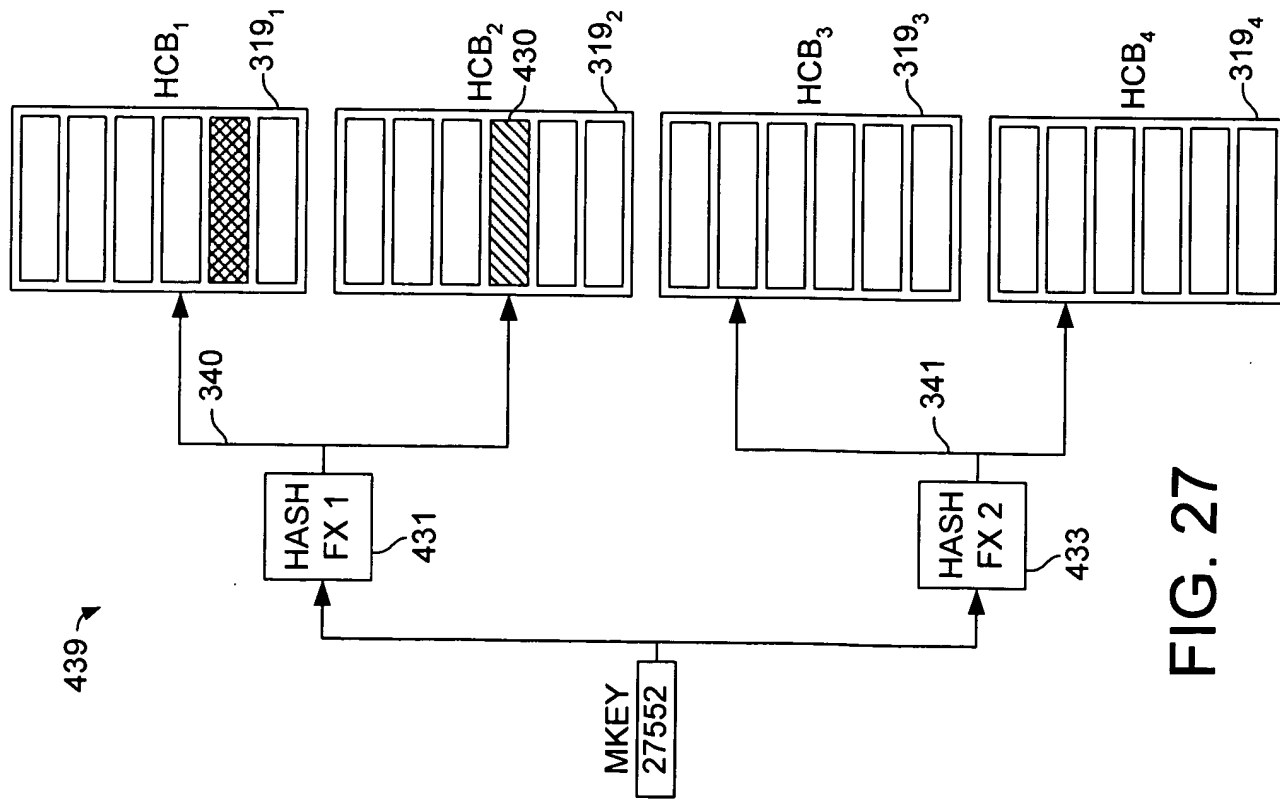


FIG. 27

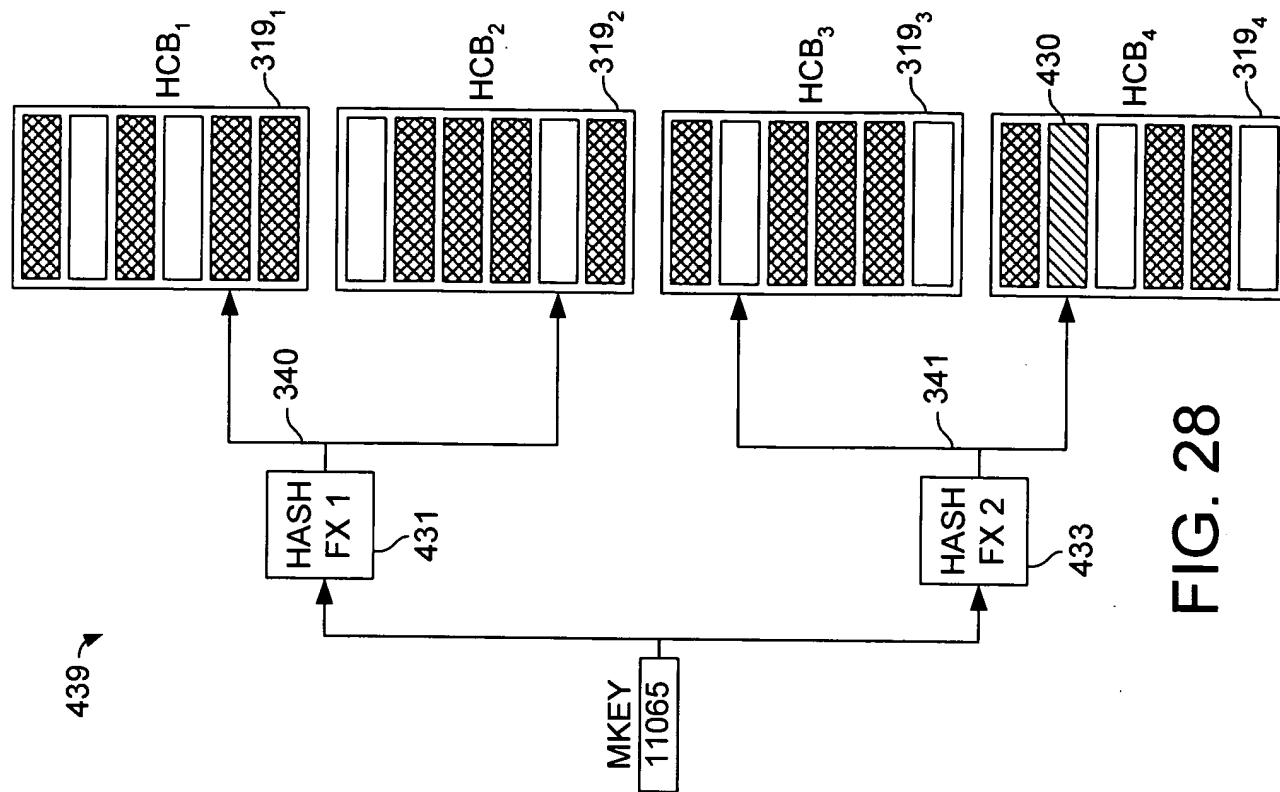


FIG. 28

FIG. 29

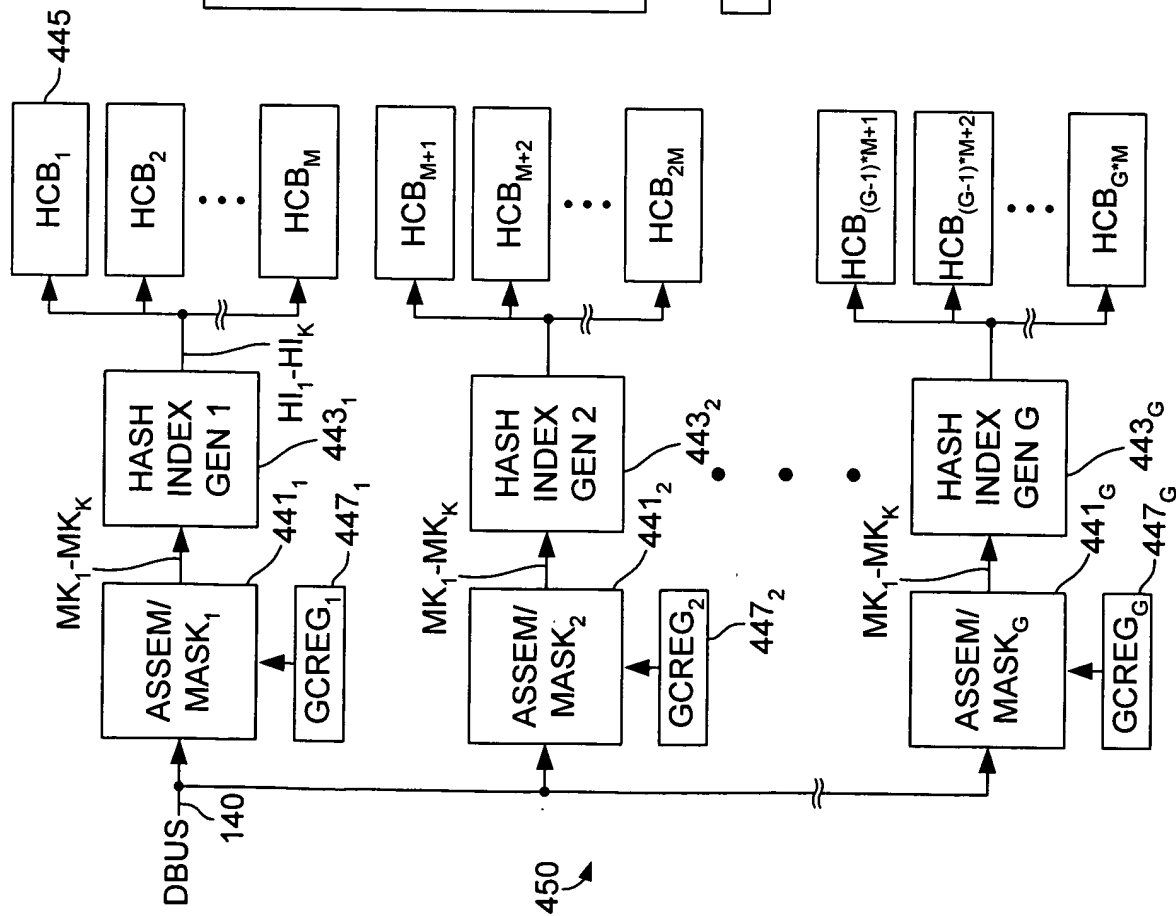
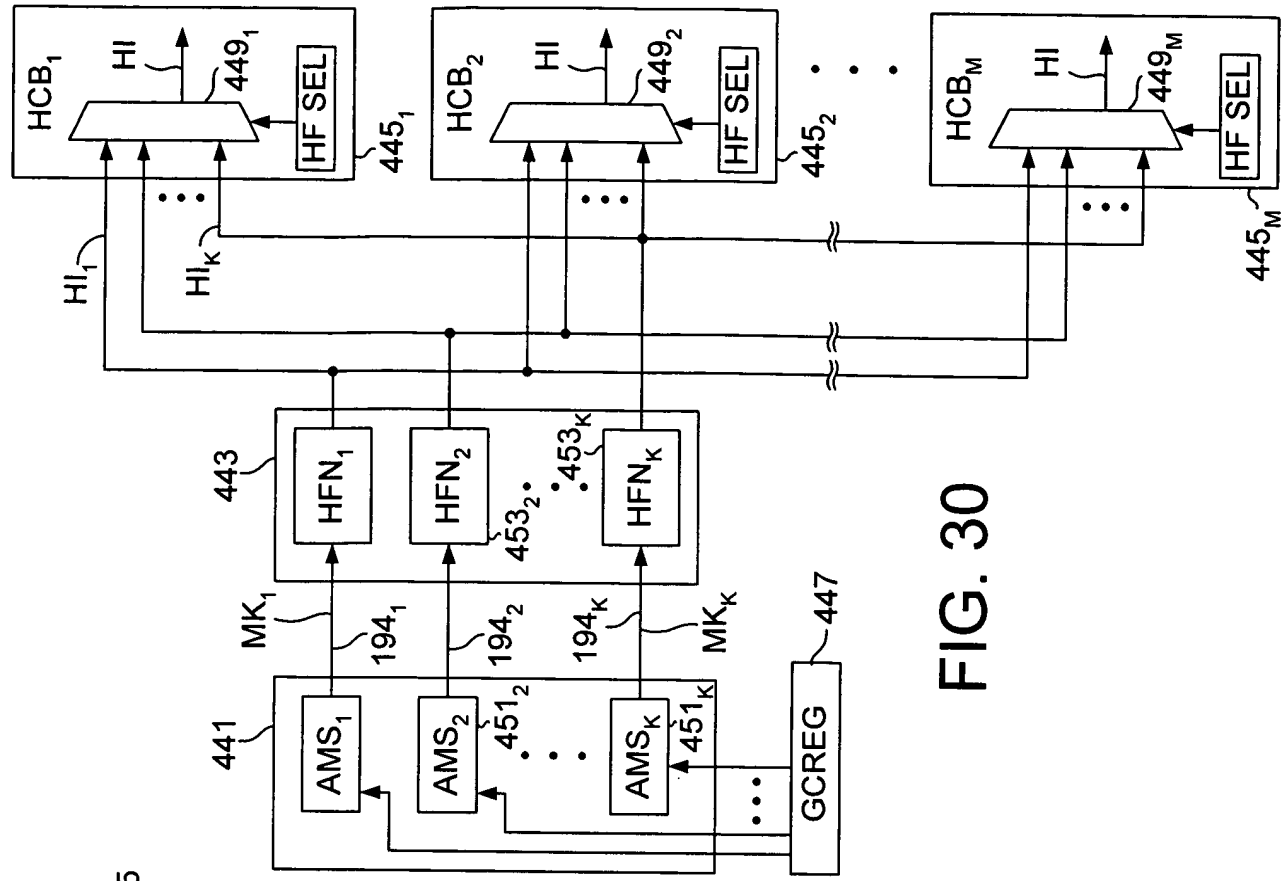


FIG. 30



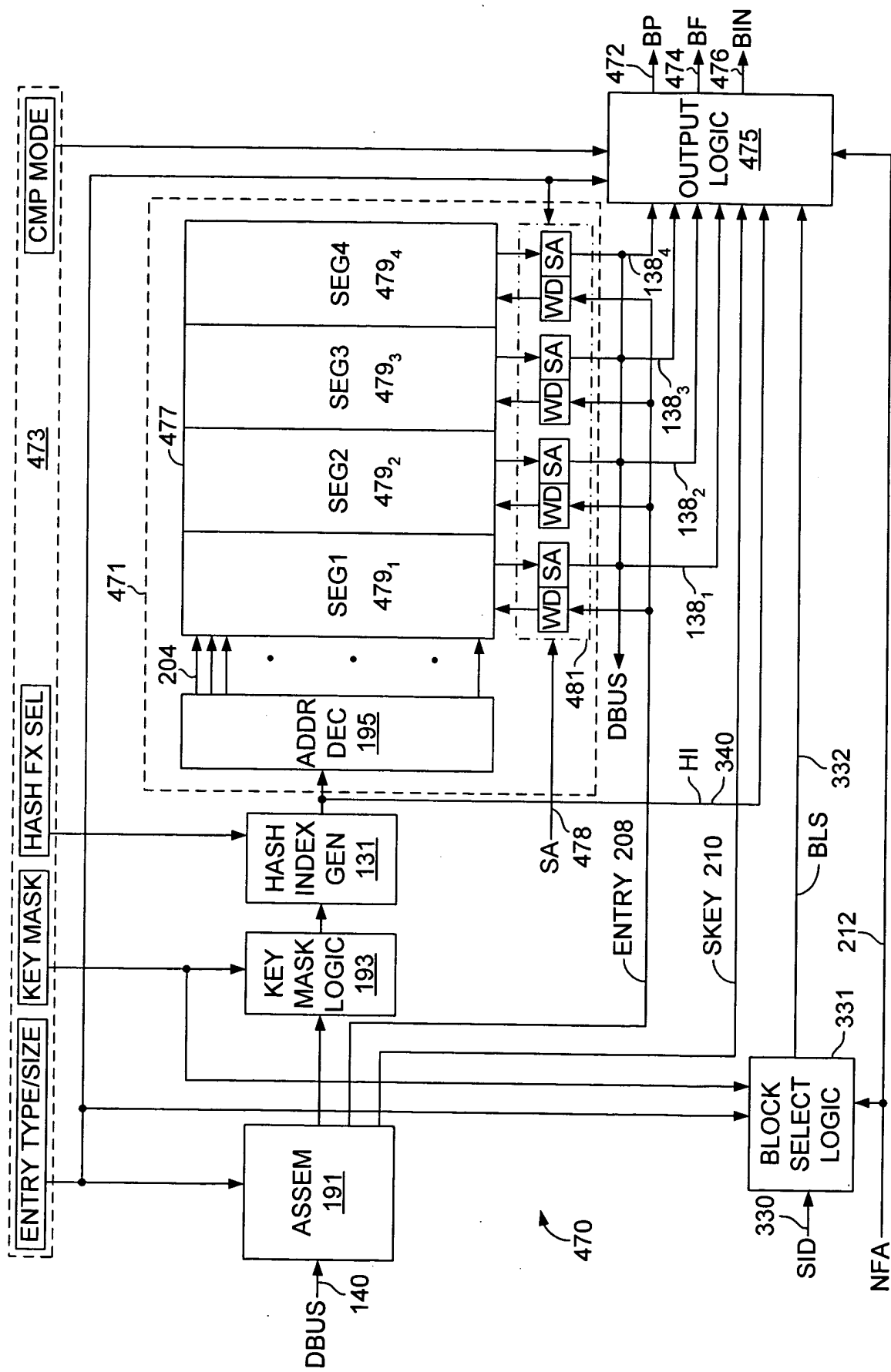


FIG. 31

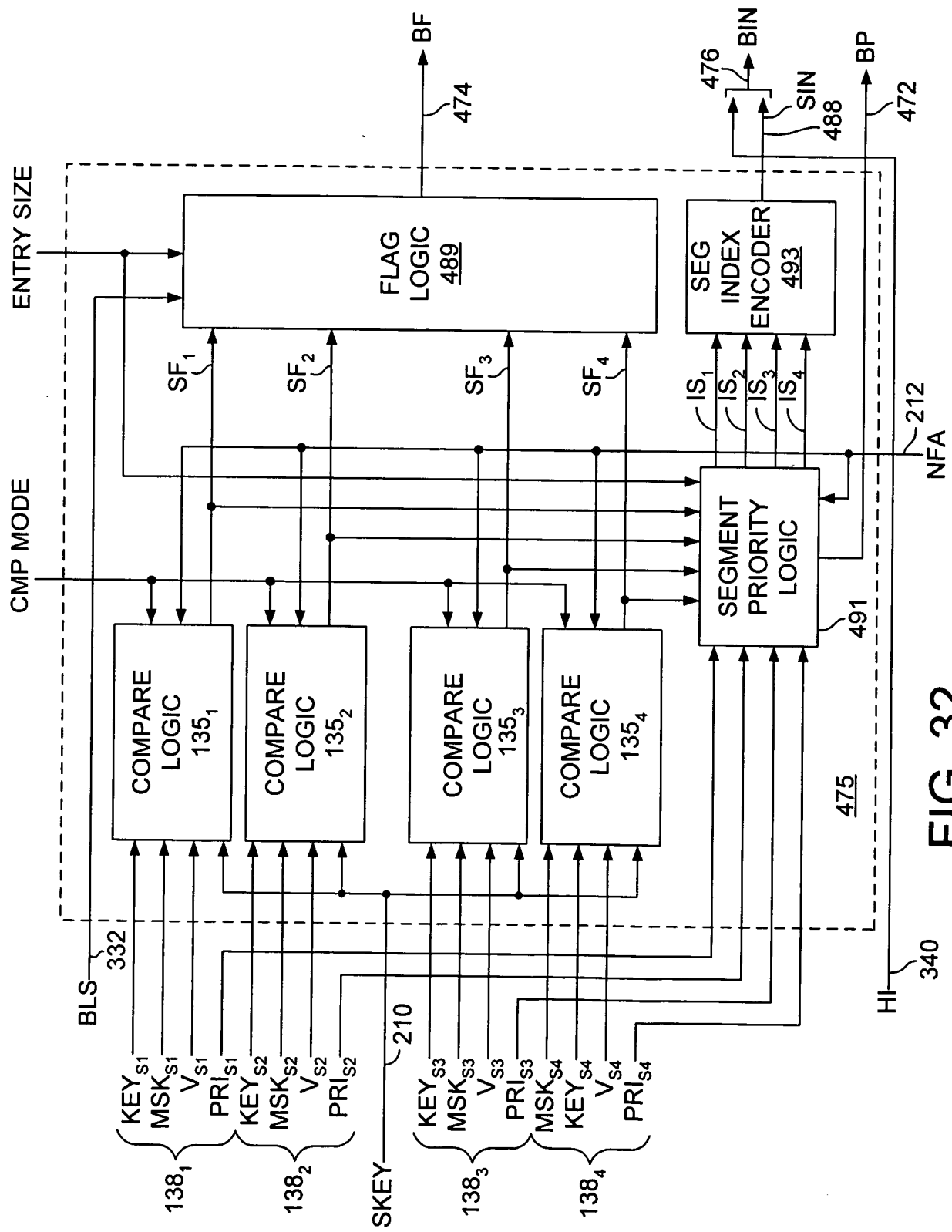


FIG. 32

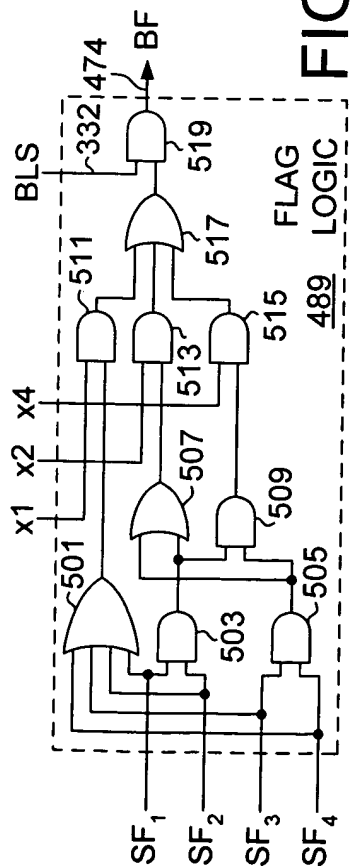


FIG. 33

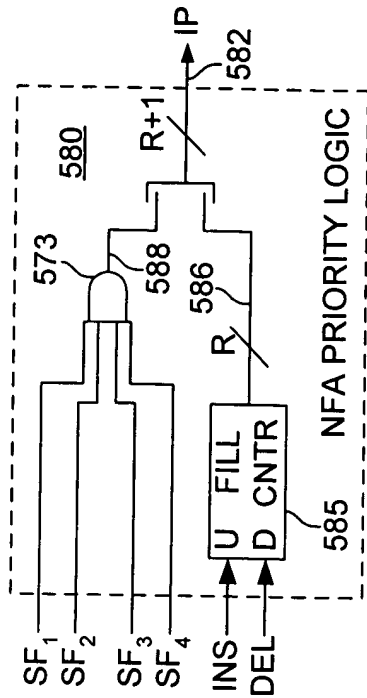


FIG. 35

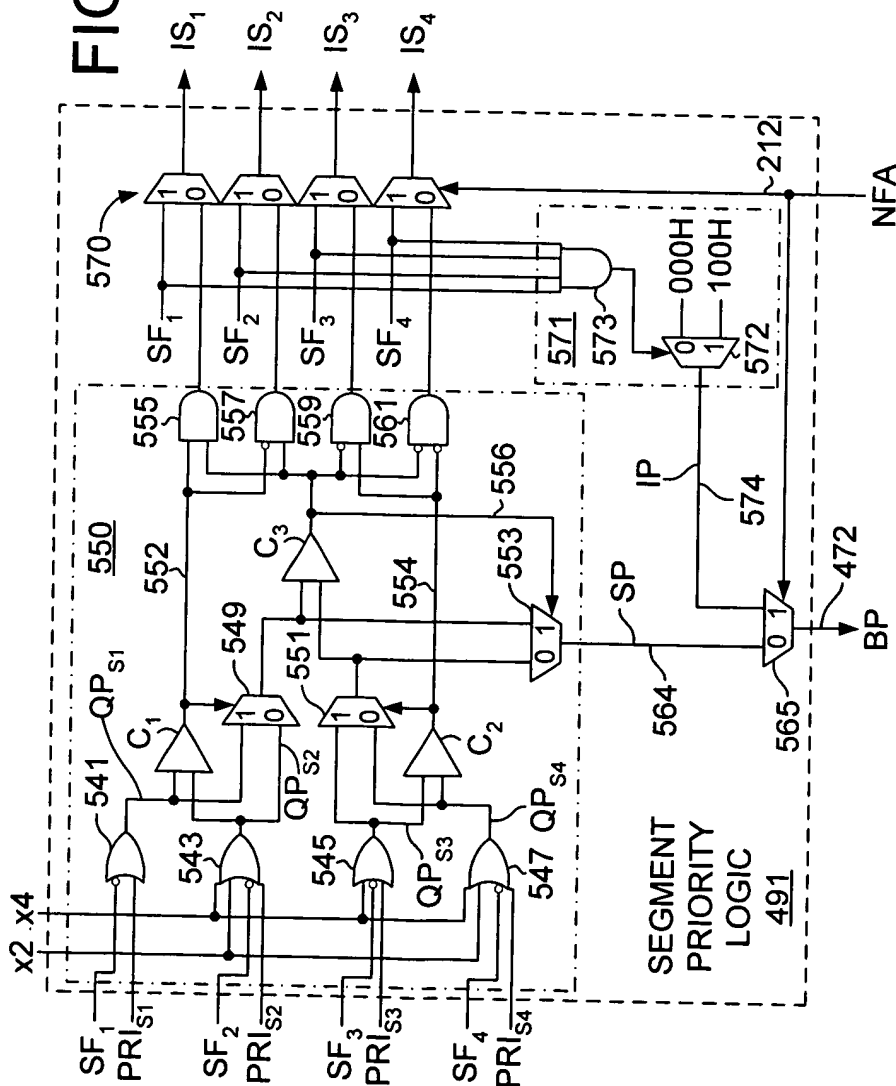


FIG. 34

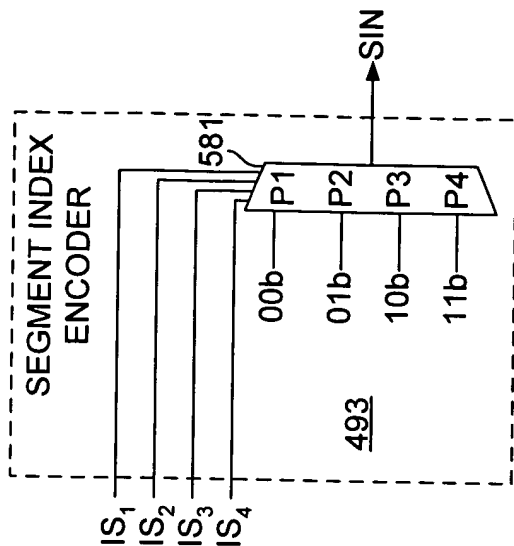


FIG. 36

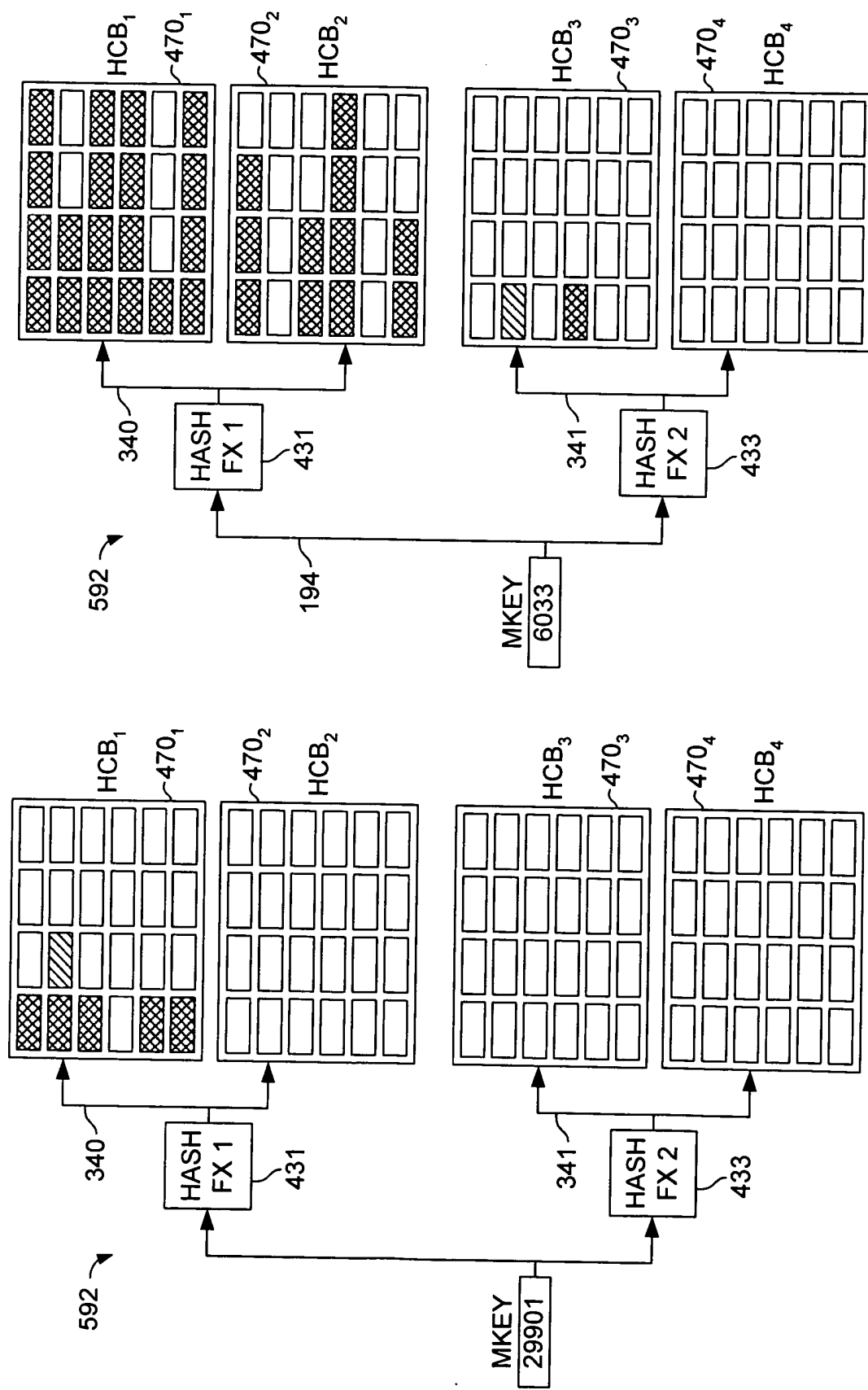


FIG. 37

FIG. 38

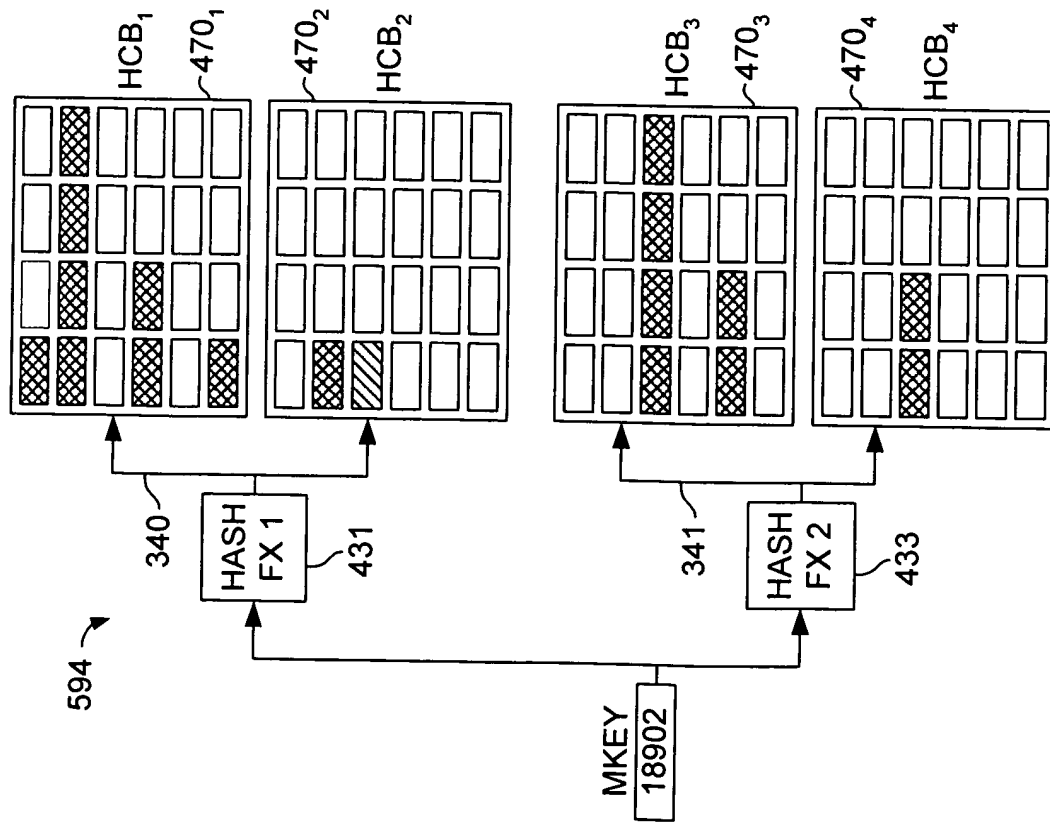


FIG. 39

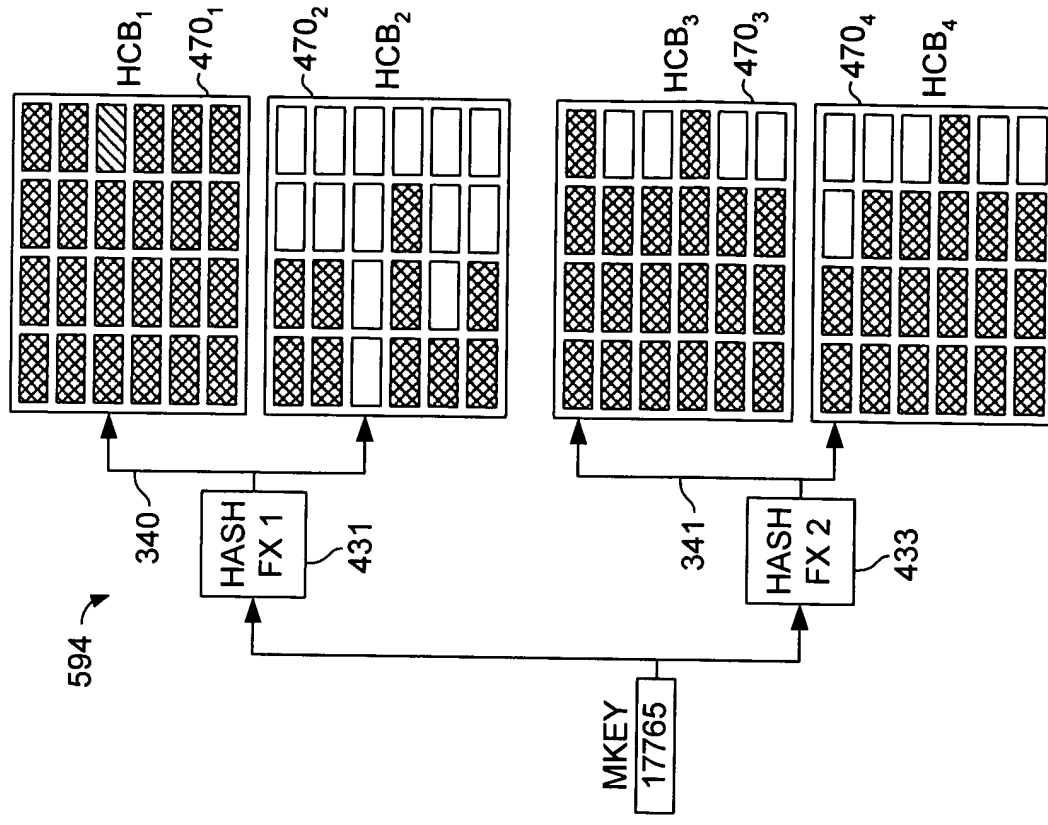


FIG. 40

FIG. 41

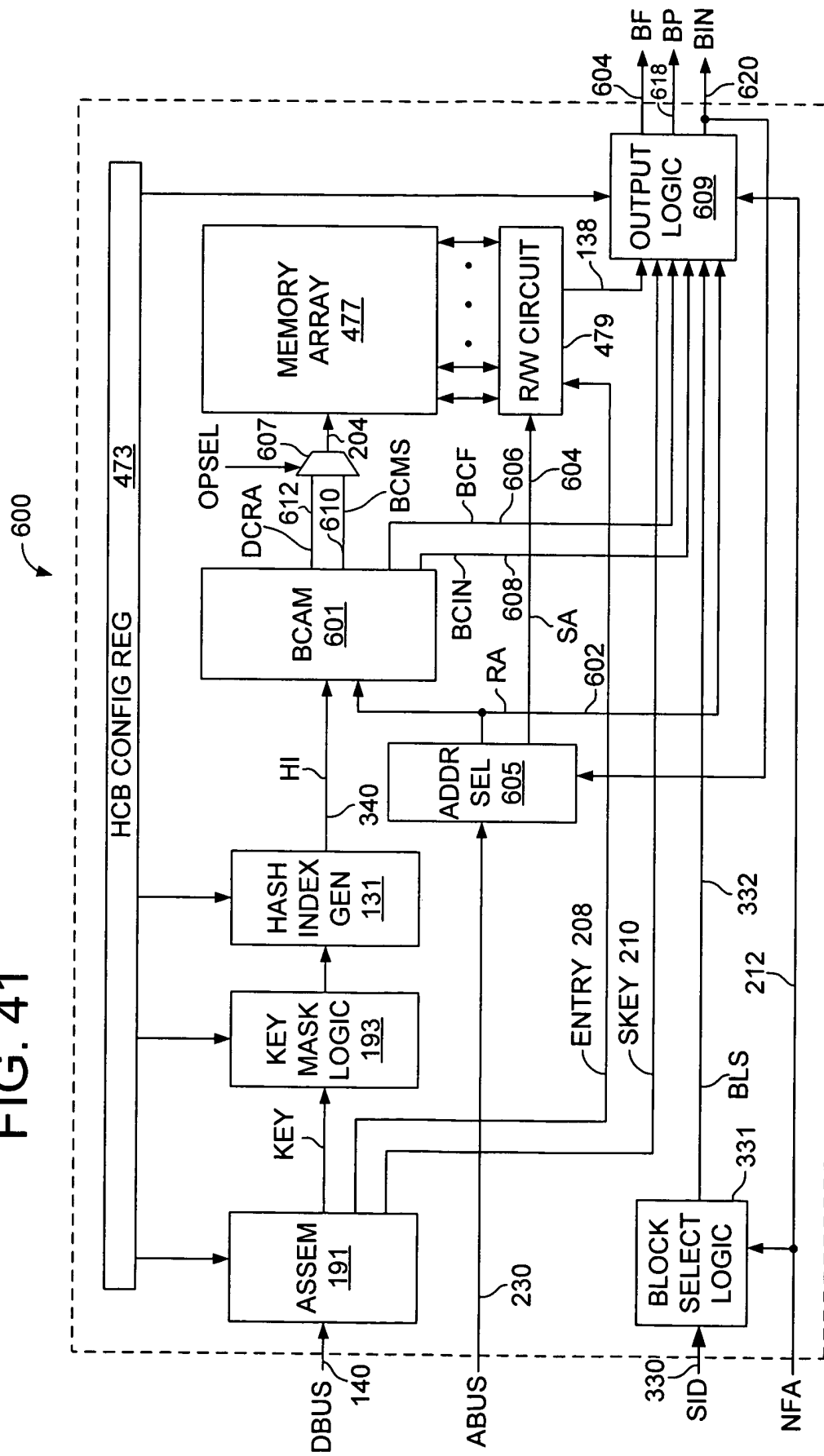
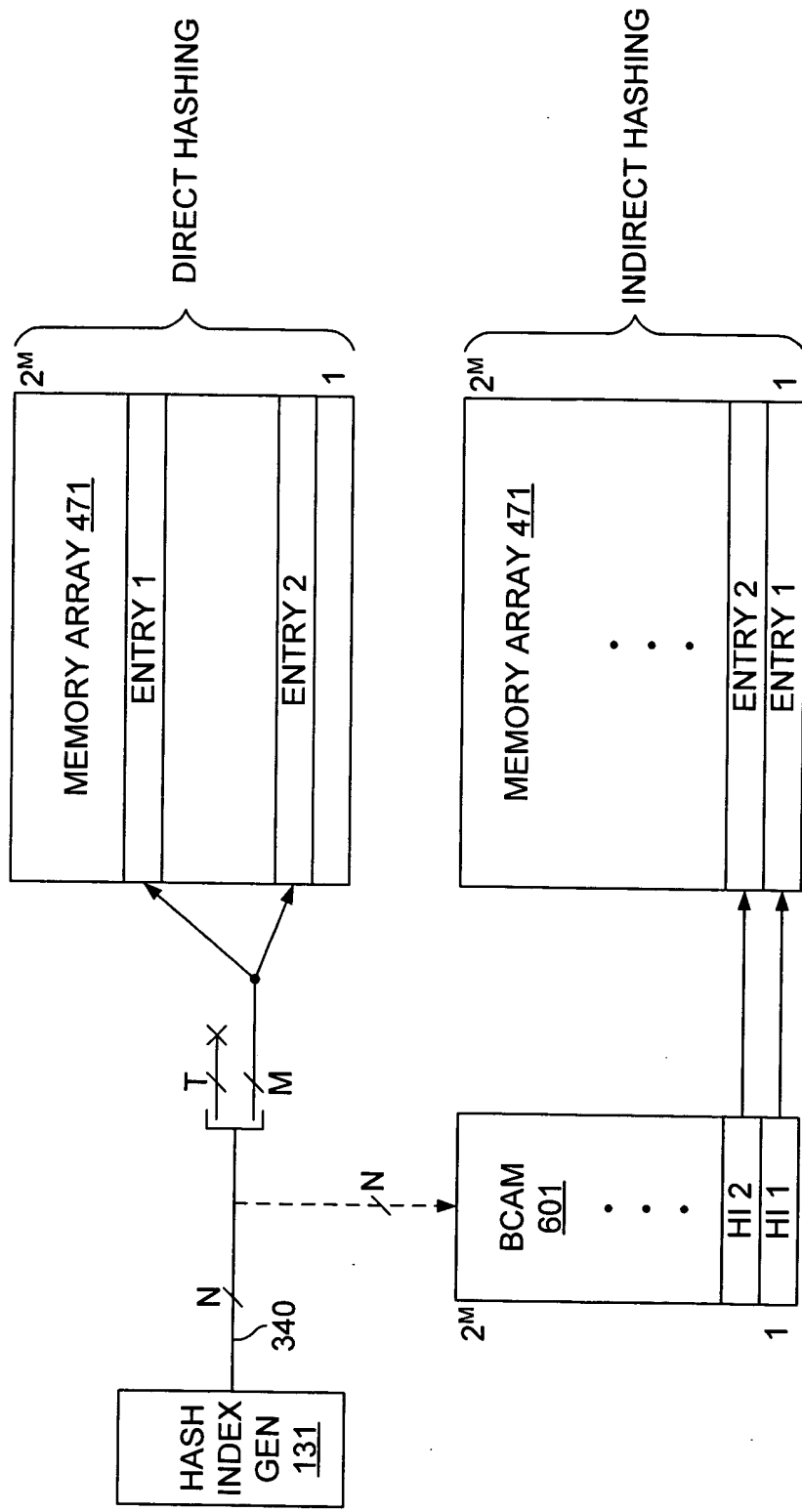


FIG. 42



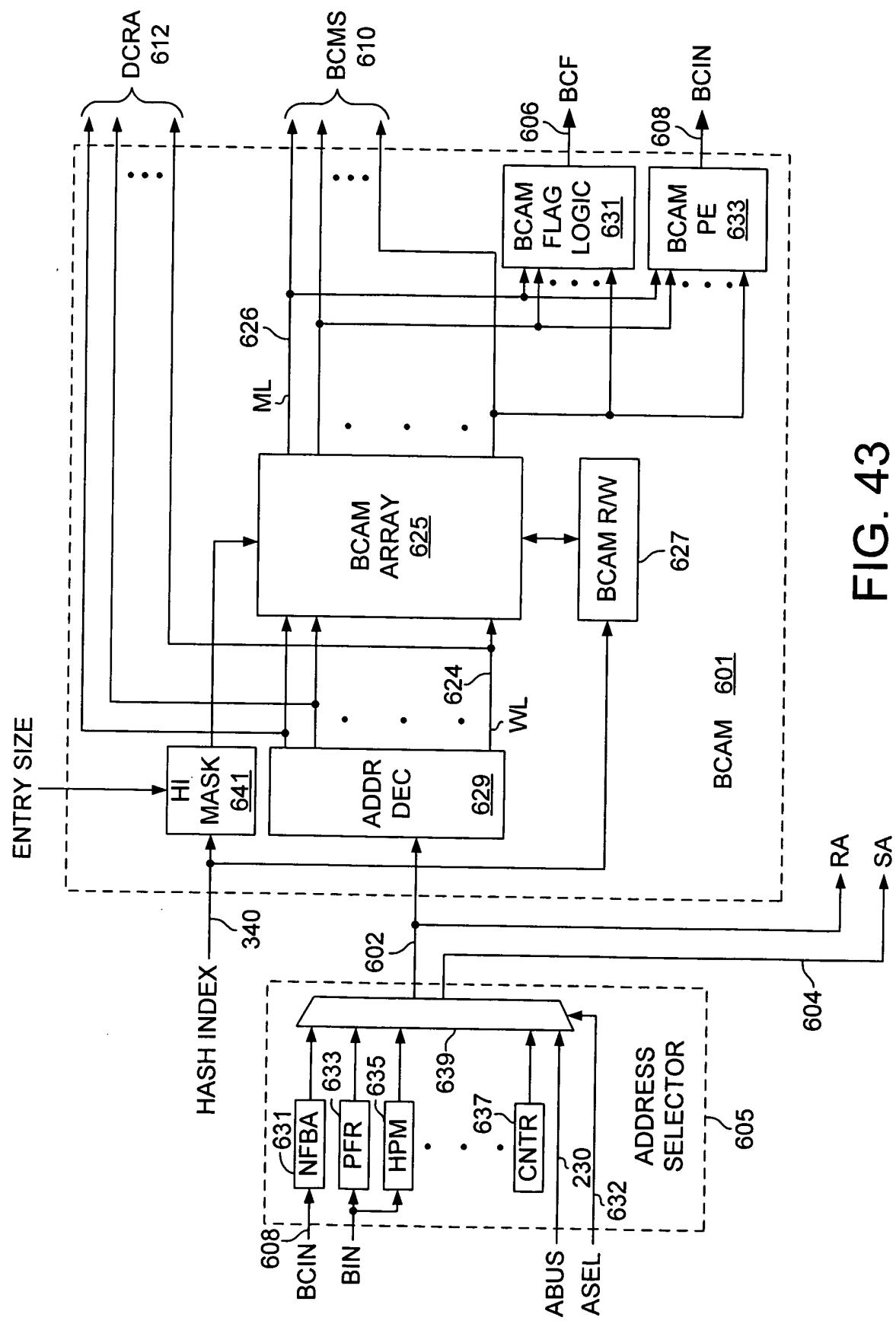
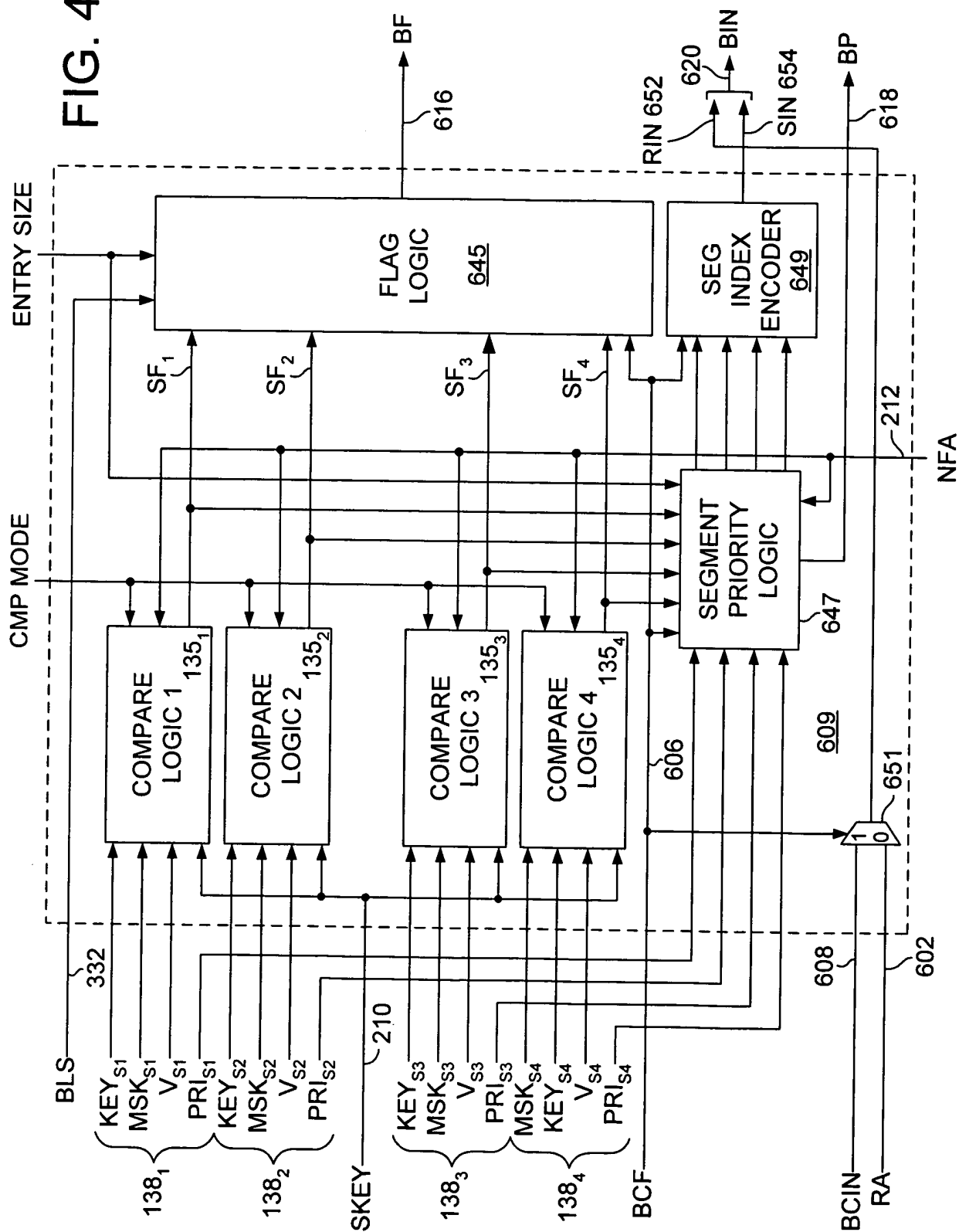


FIG. 43

FIG. 44



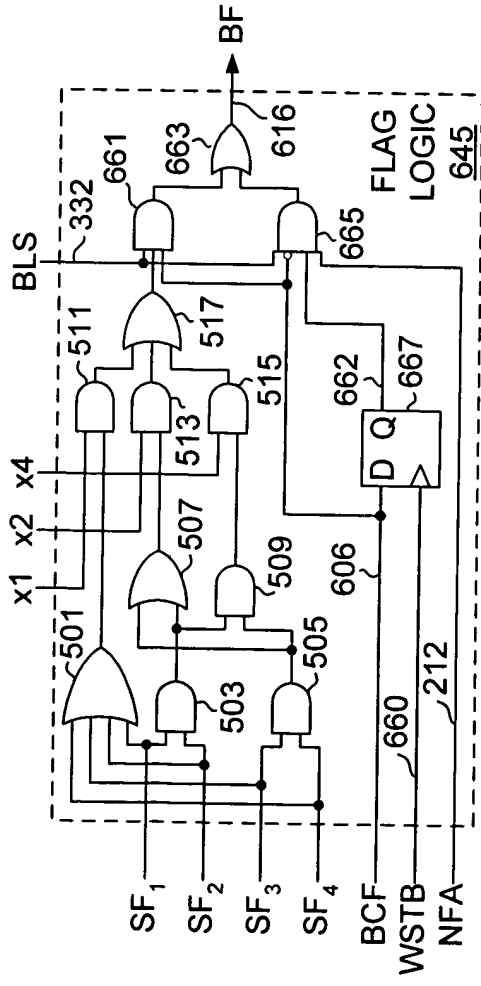


FIG. 45

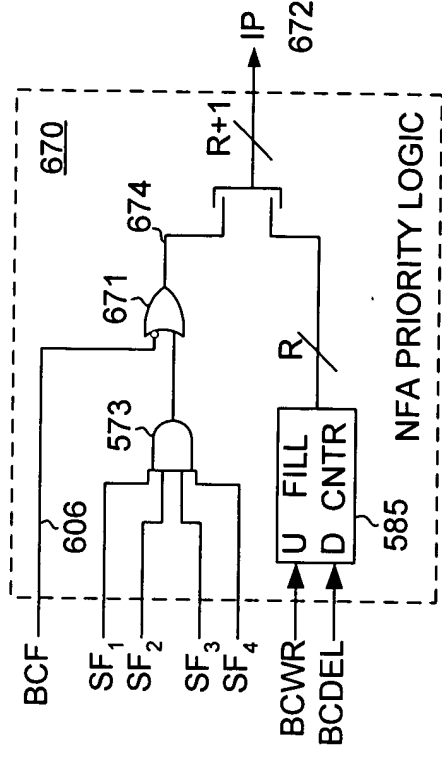


FIG. 46

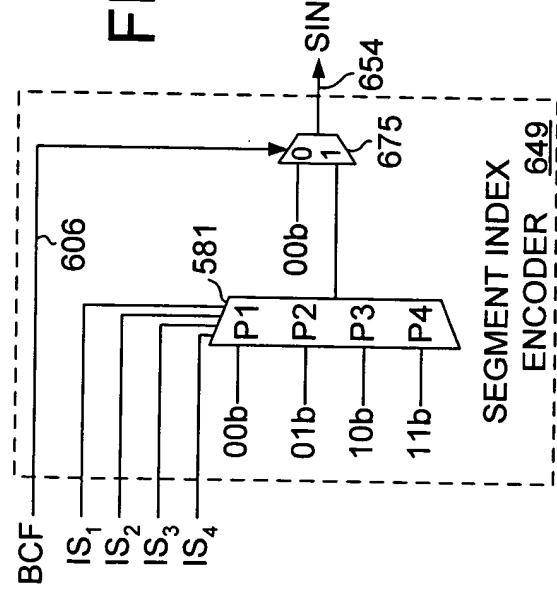


FIG. 47

FIG. 48

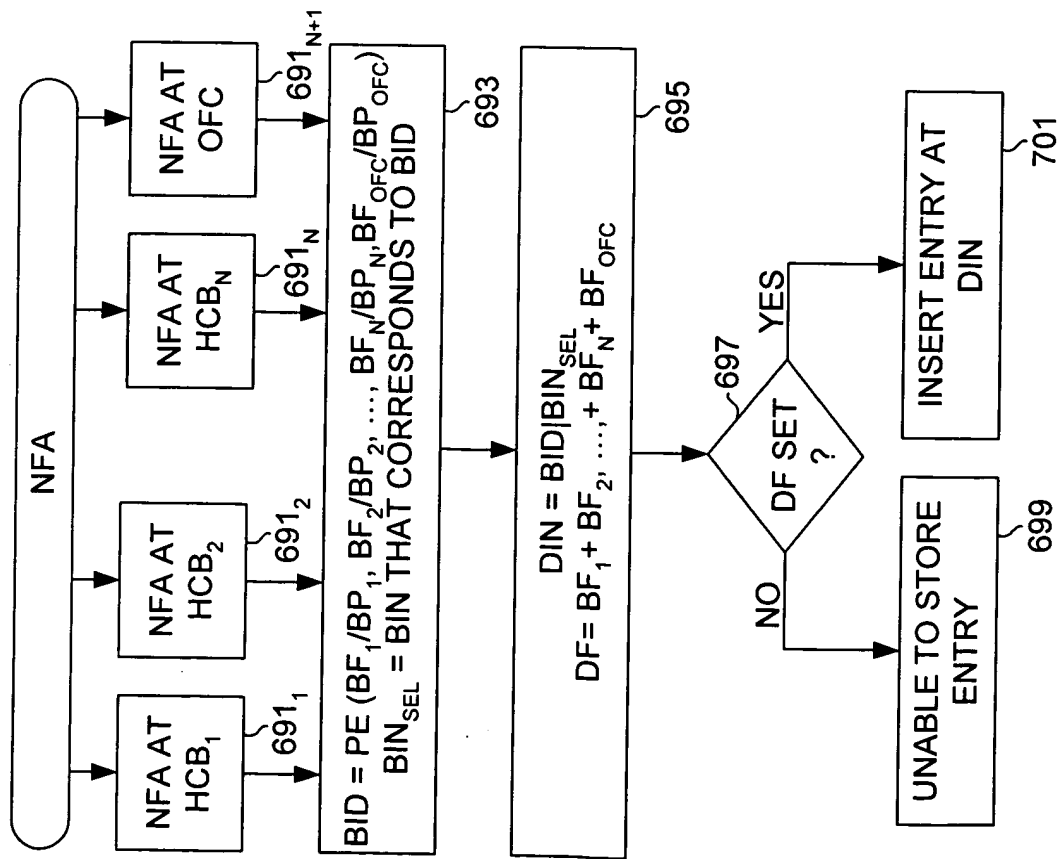


FIG. 49

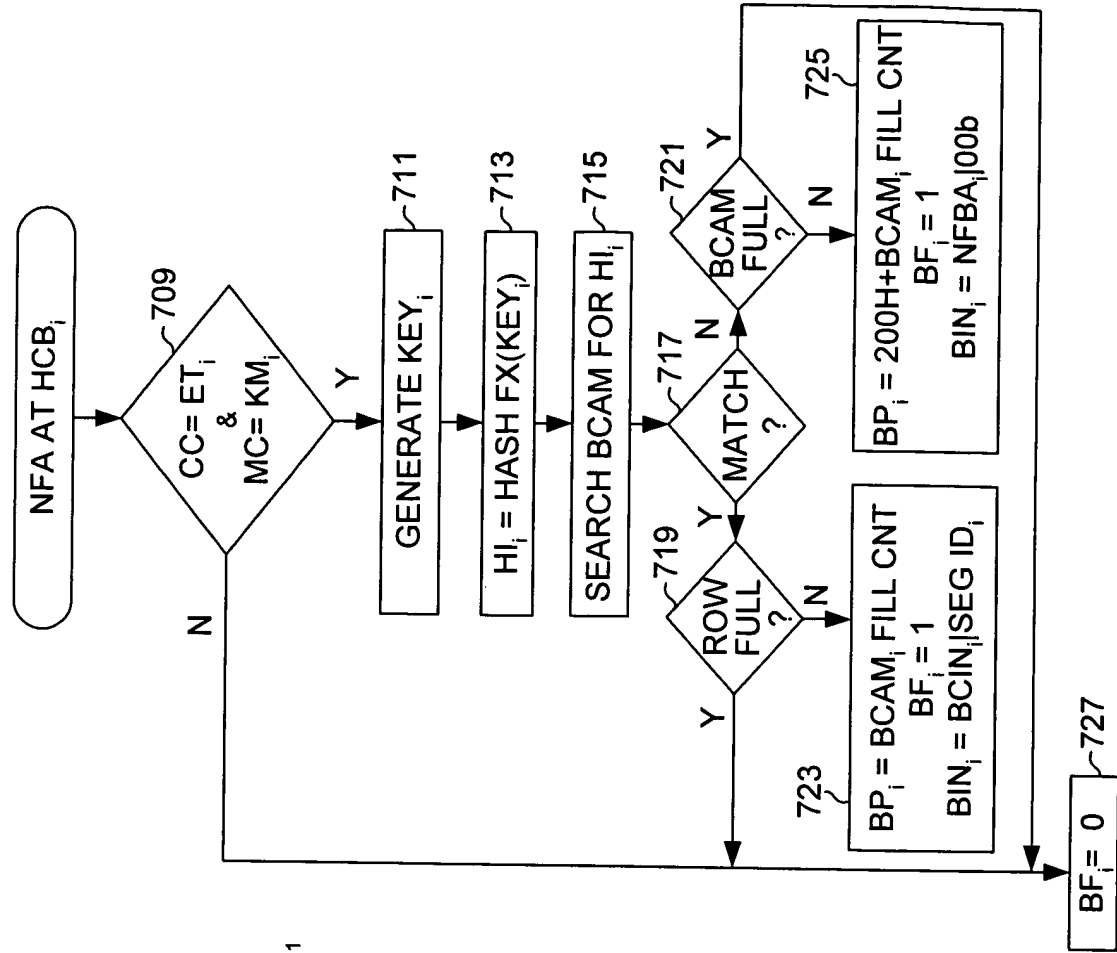


FIG. 50

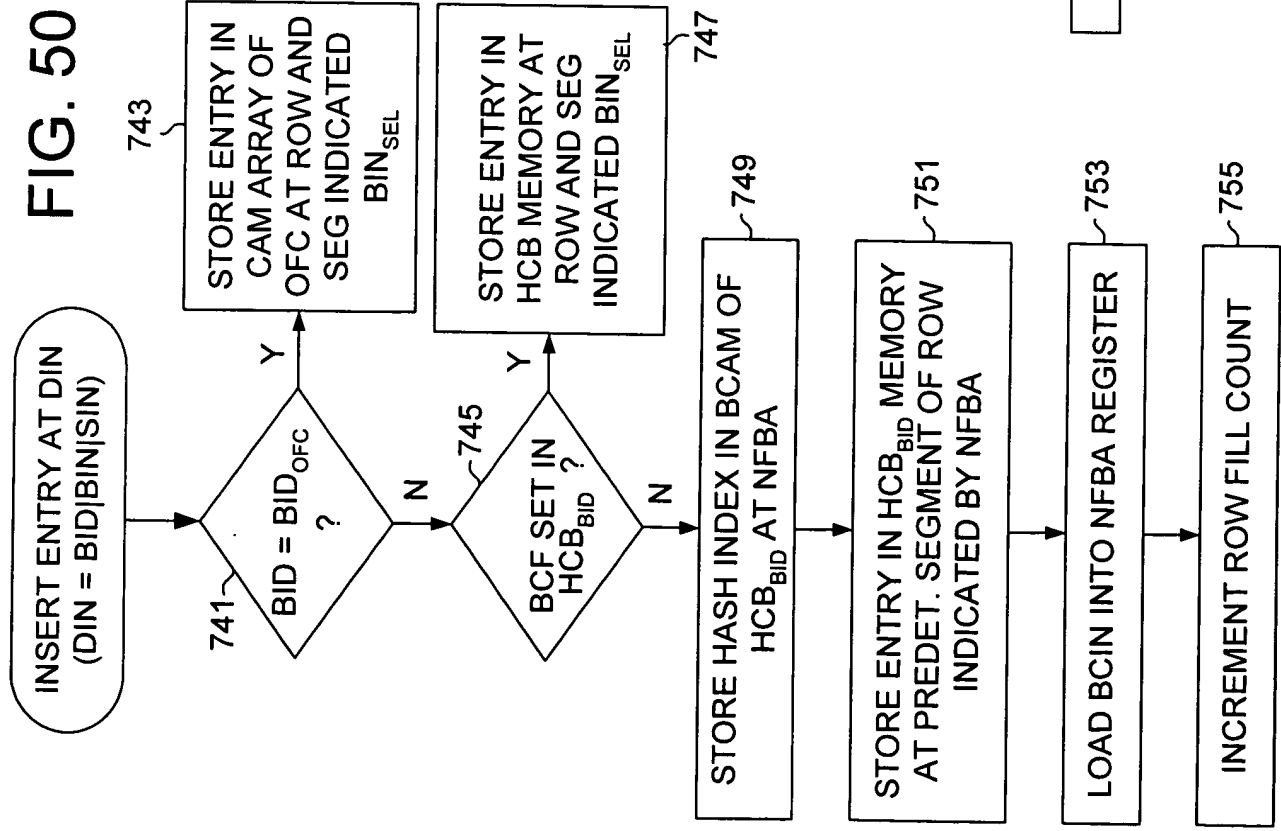
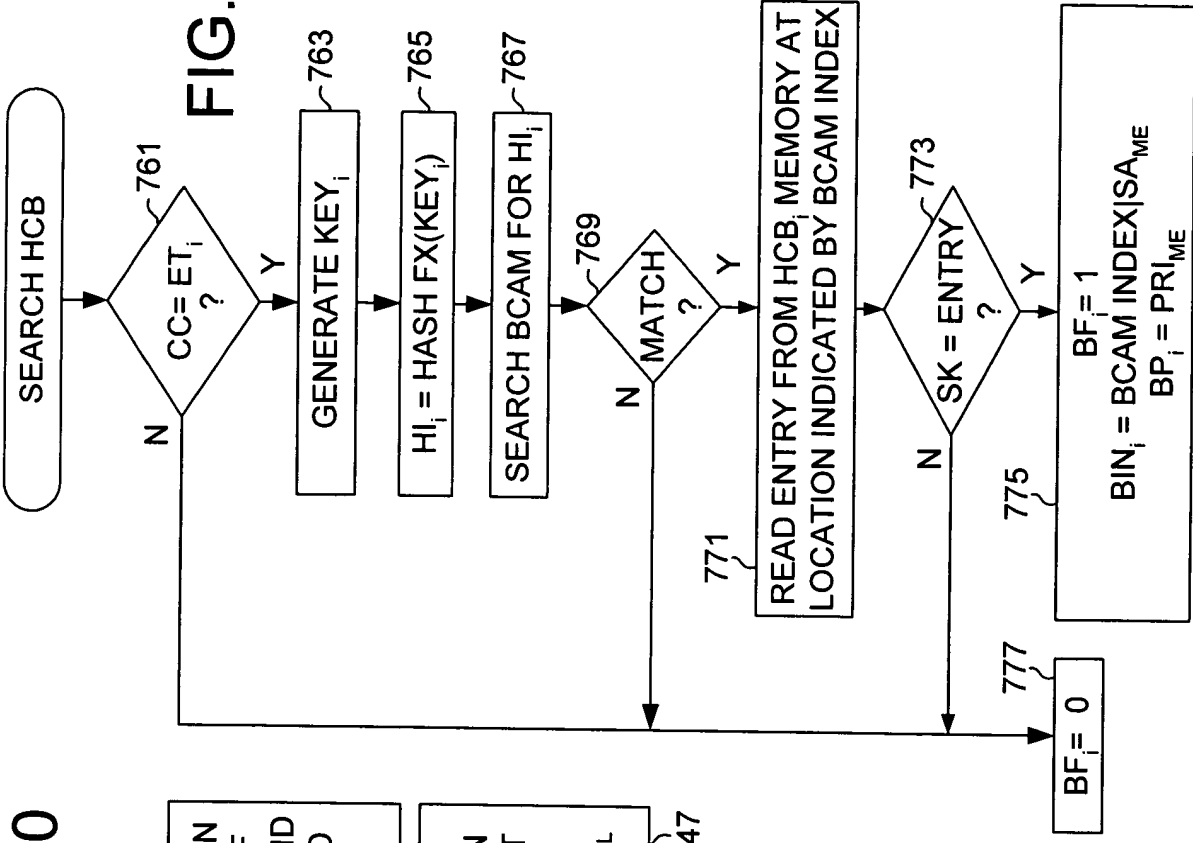


FIG. 51



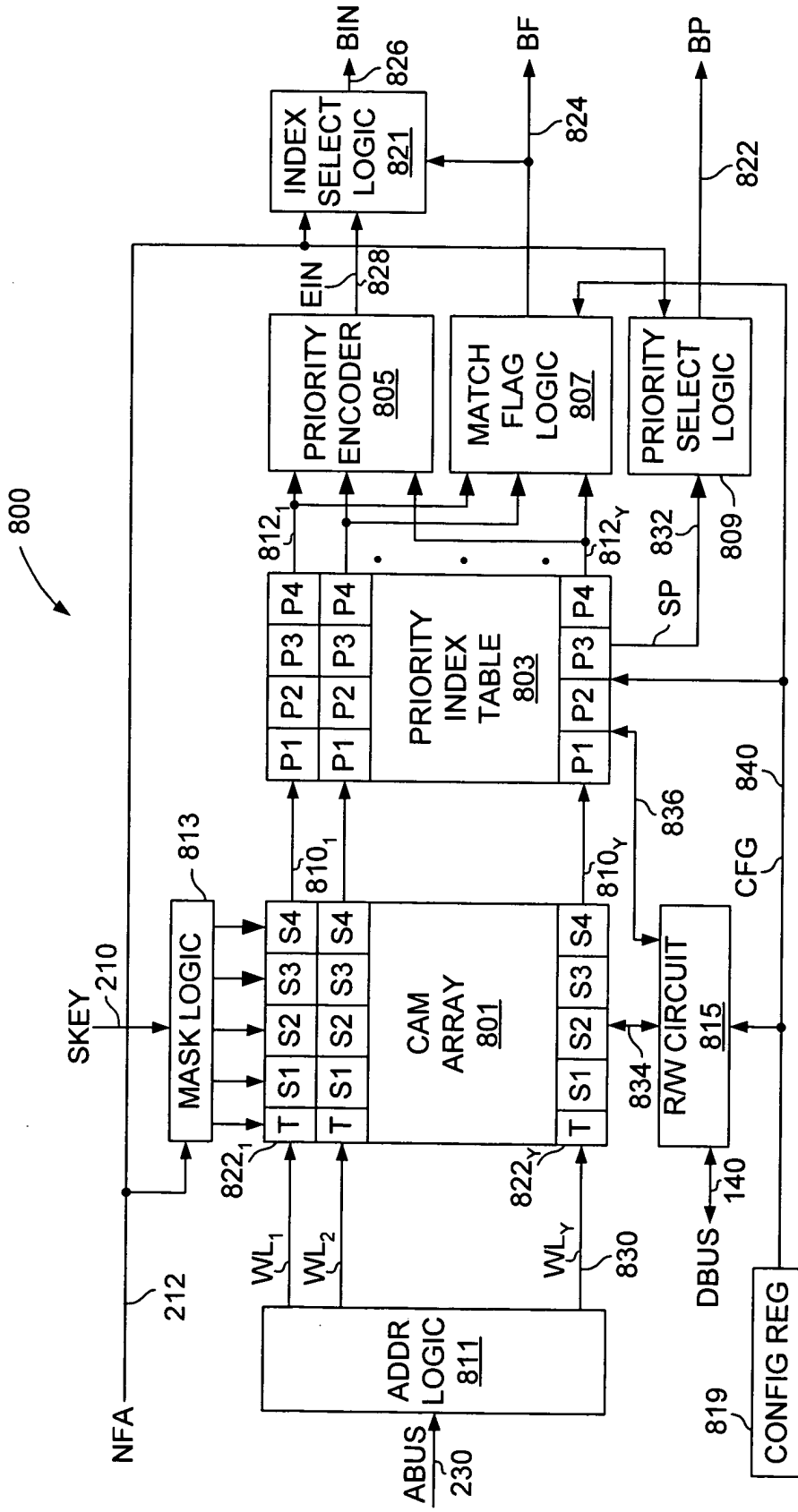


FIG. 52

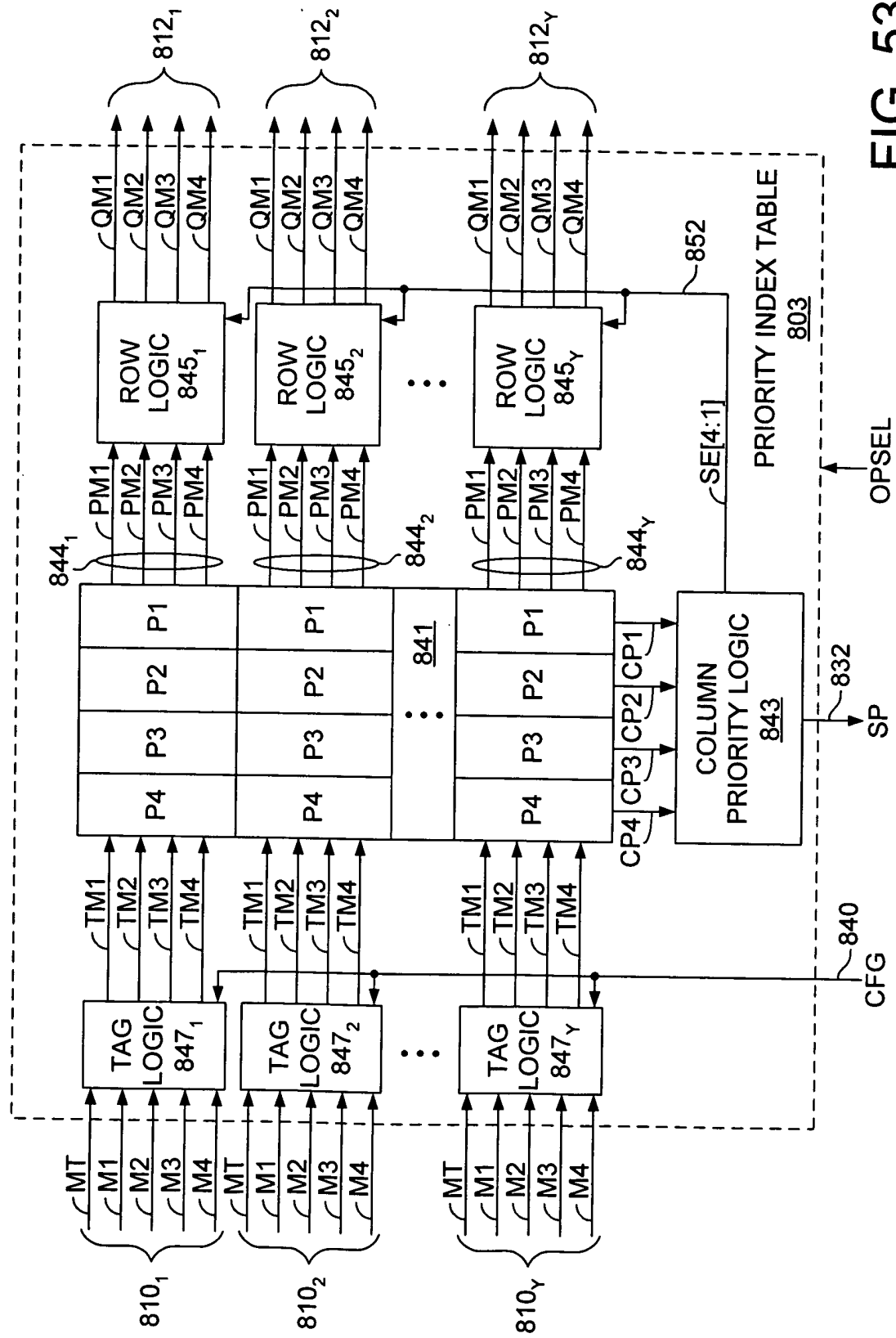


FIG. 53

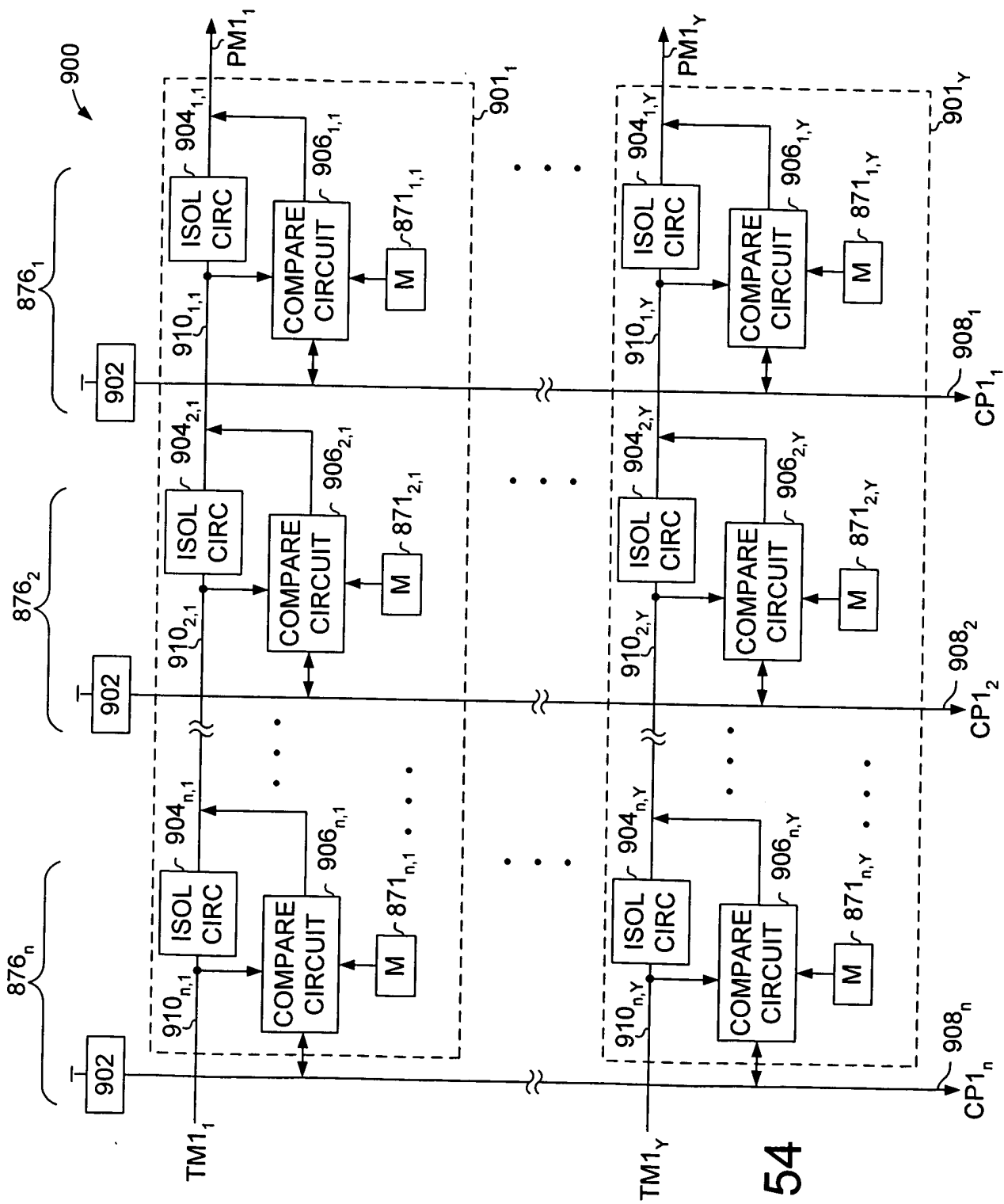


FIG. 54

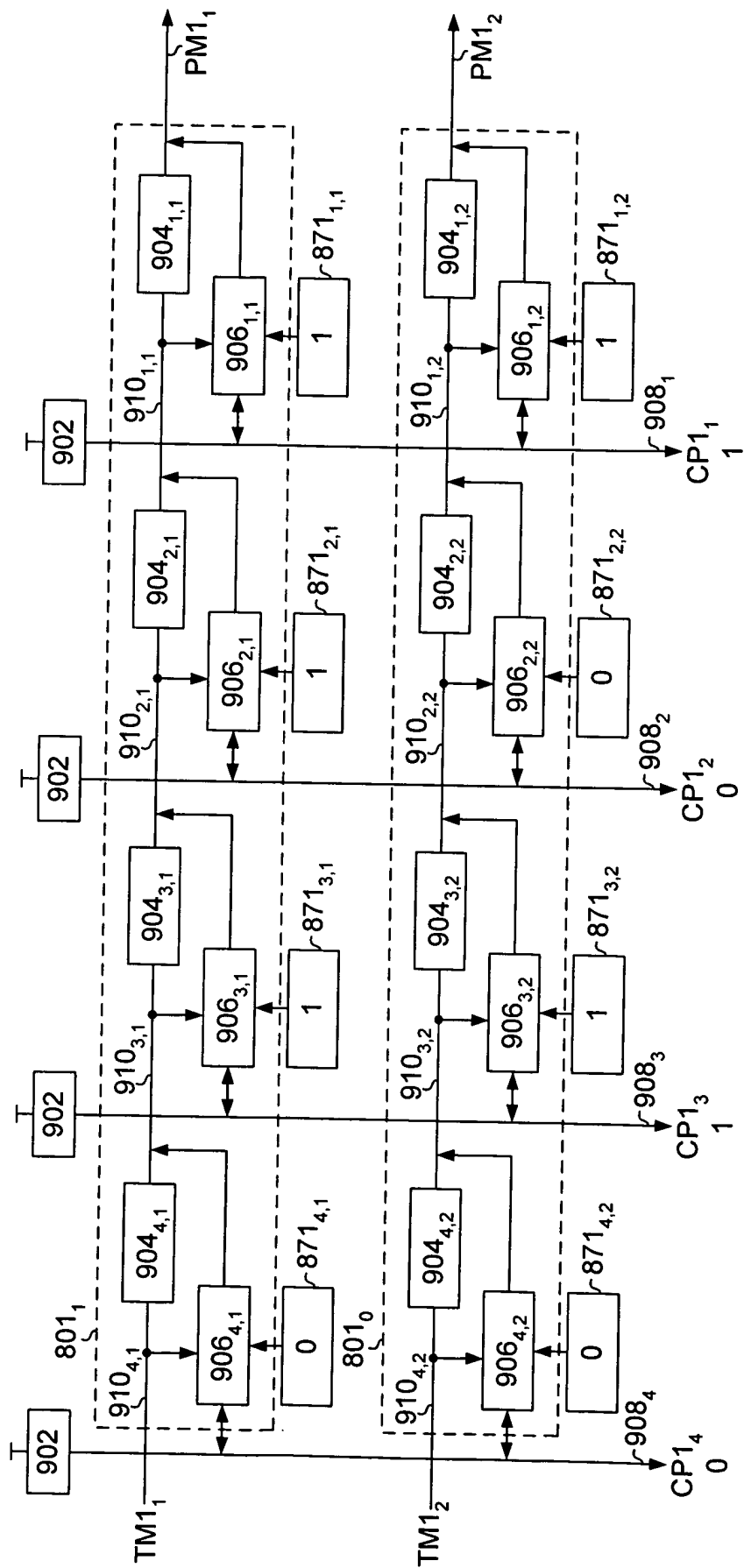


FIG. 55

FIG. 56

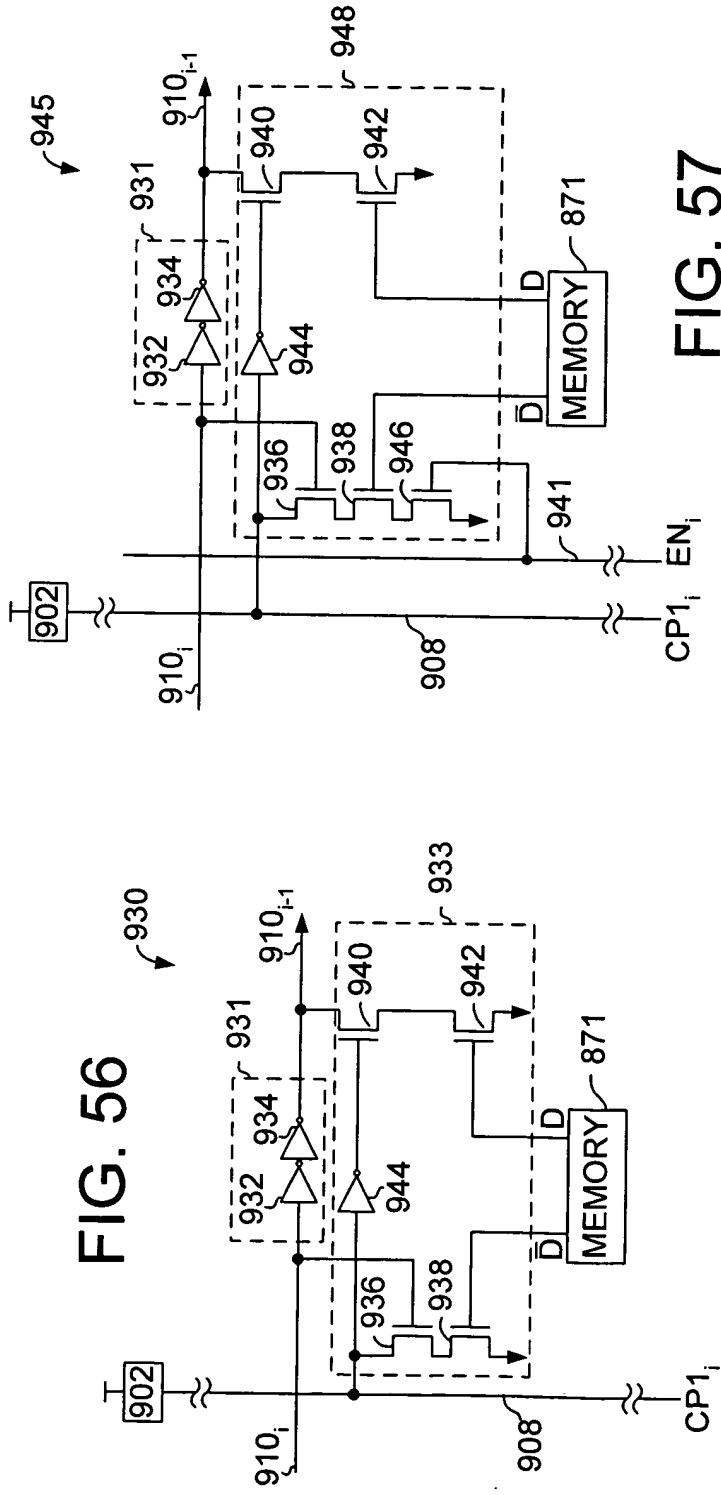


FIG. 57

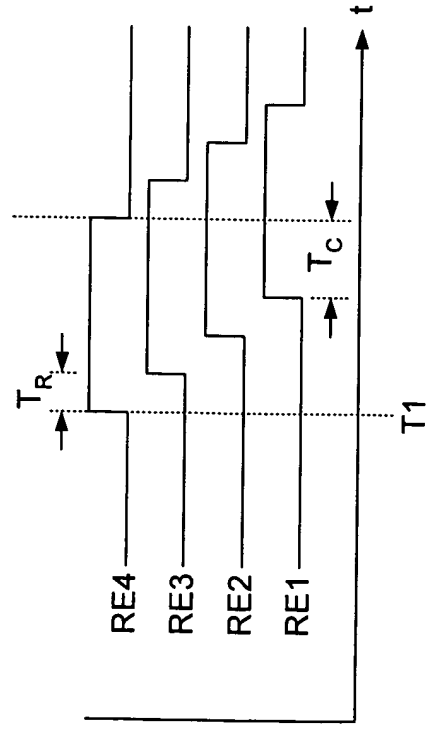


FIG. 58

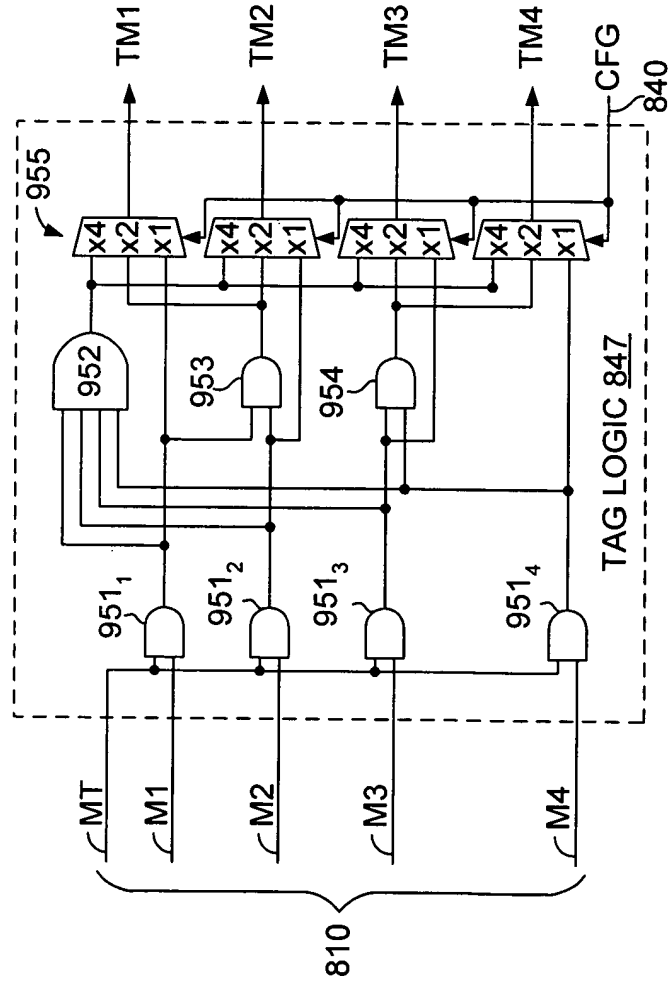


FIG. 59

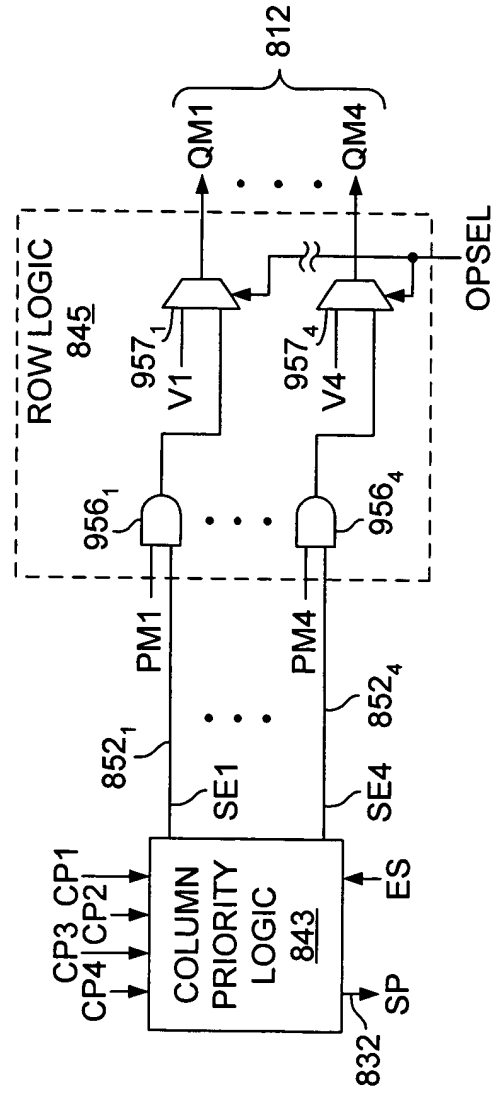


FIG. 60

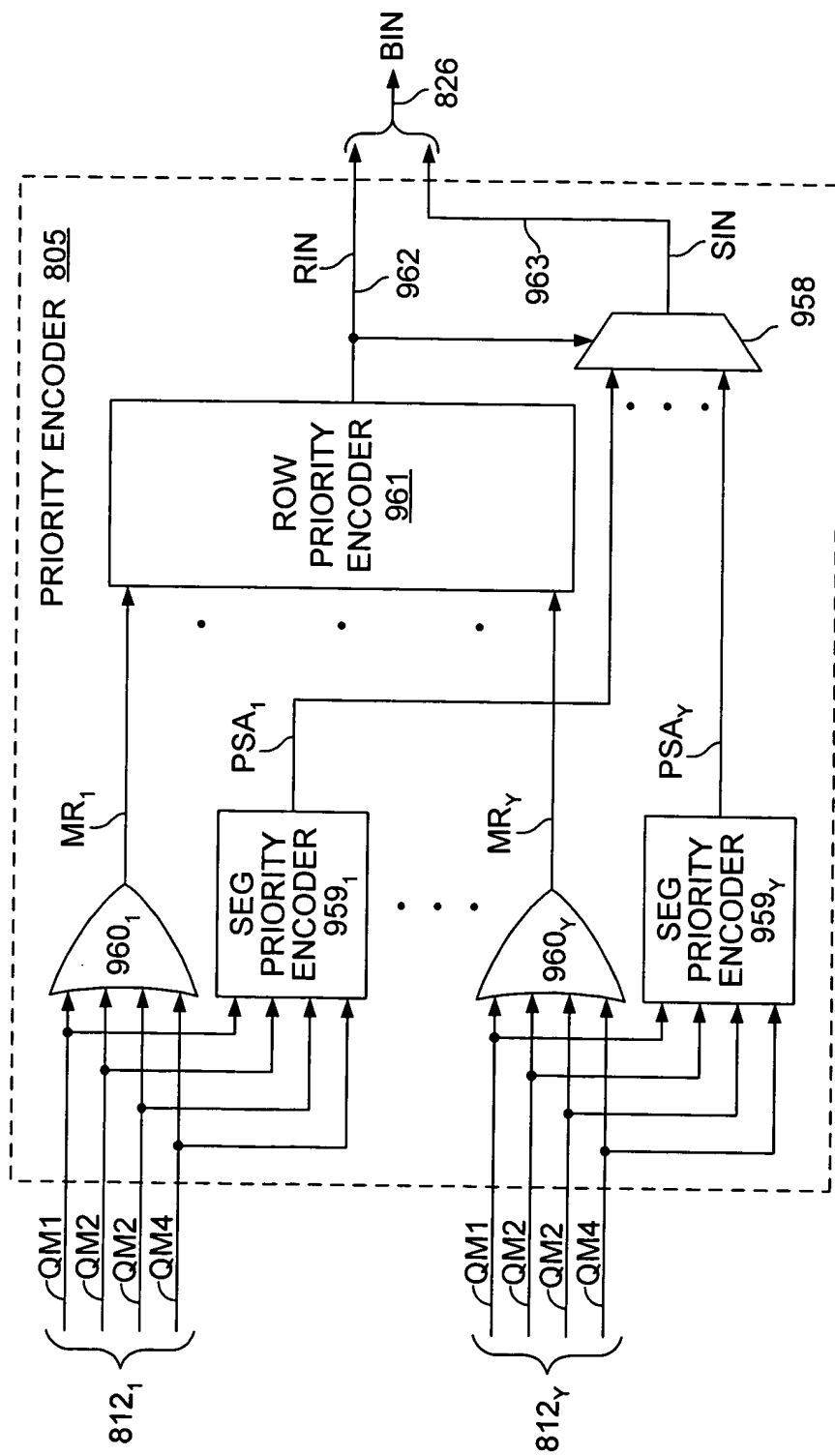


FIG. 61

FIG. 62

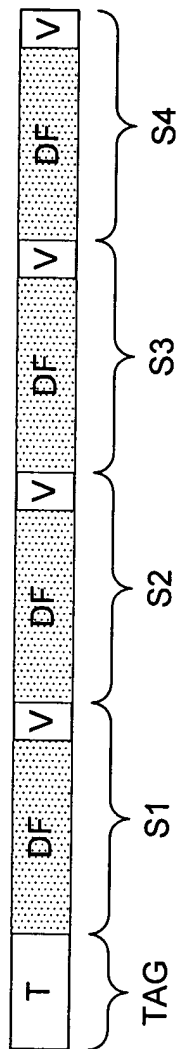


FIG. 63

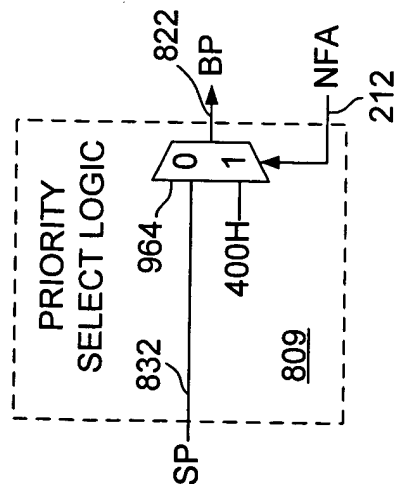


FIG. 64

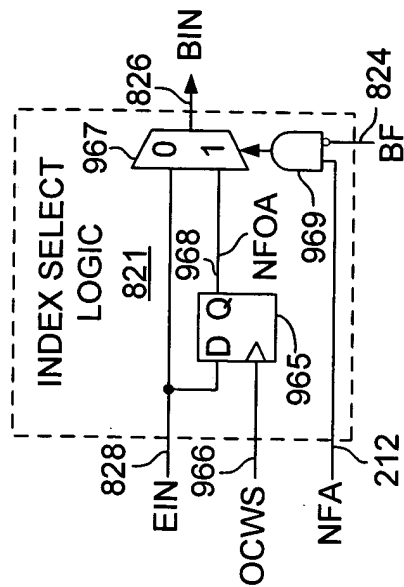


FIG. 65

